

FIG. 1

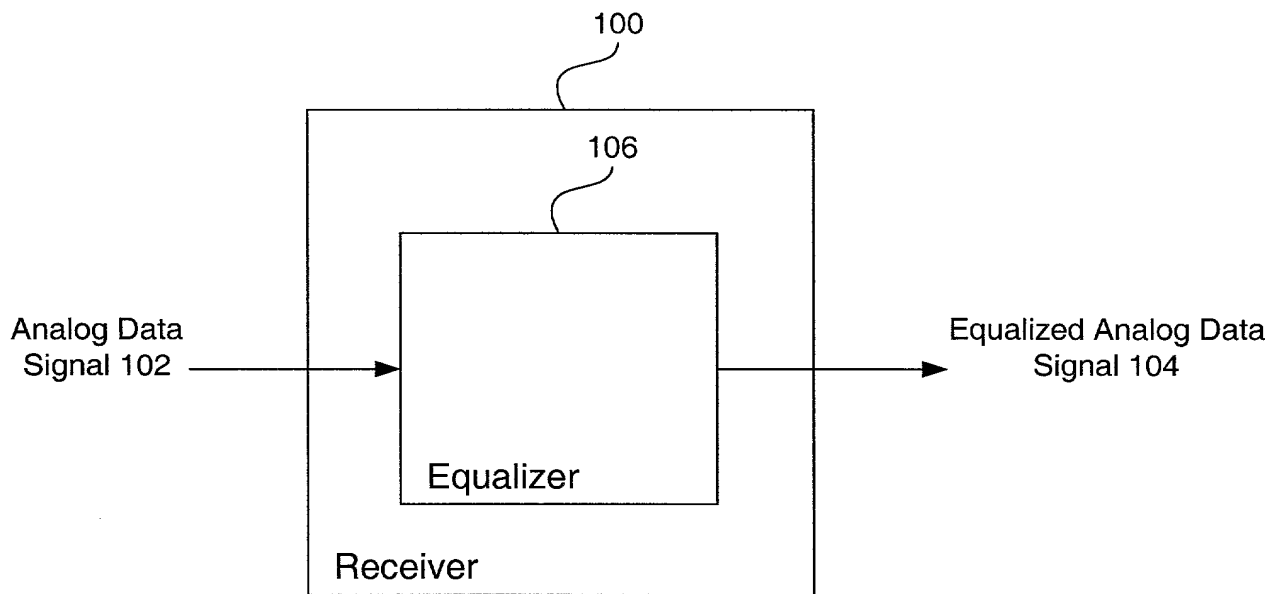


FIG. 1

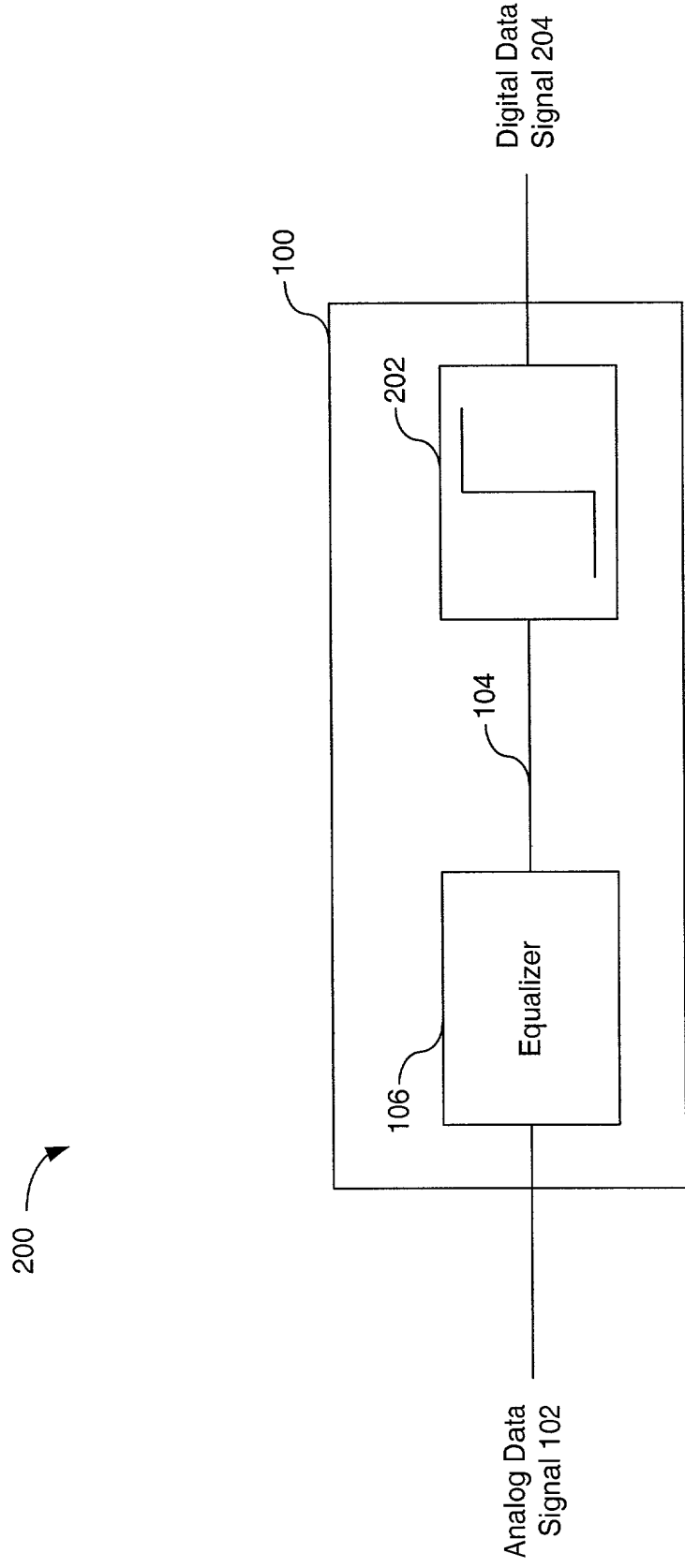
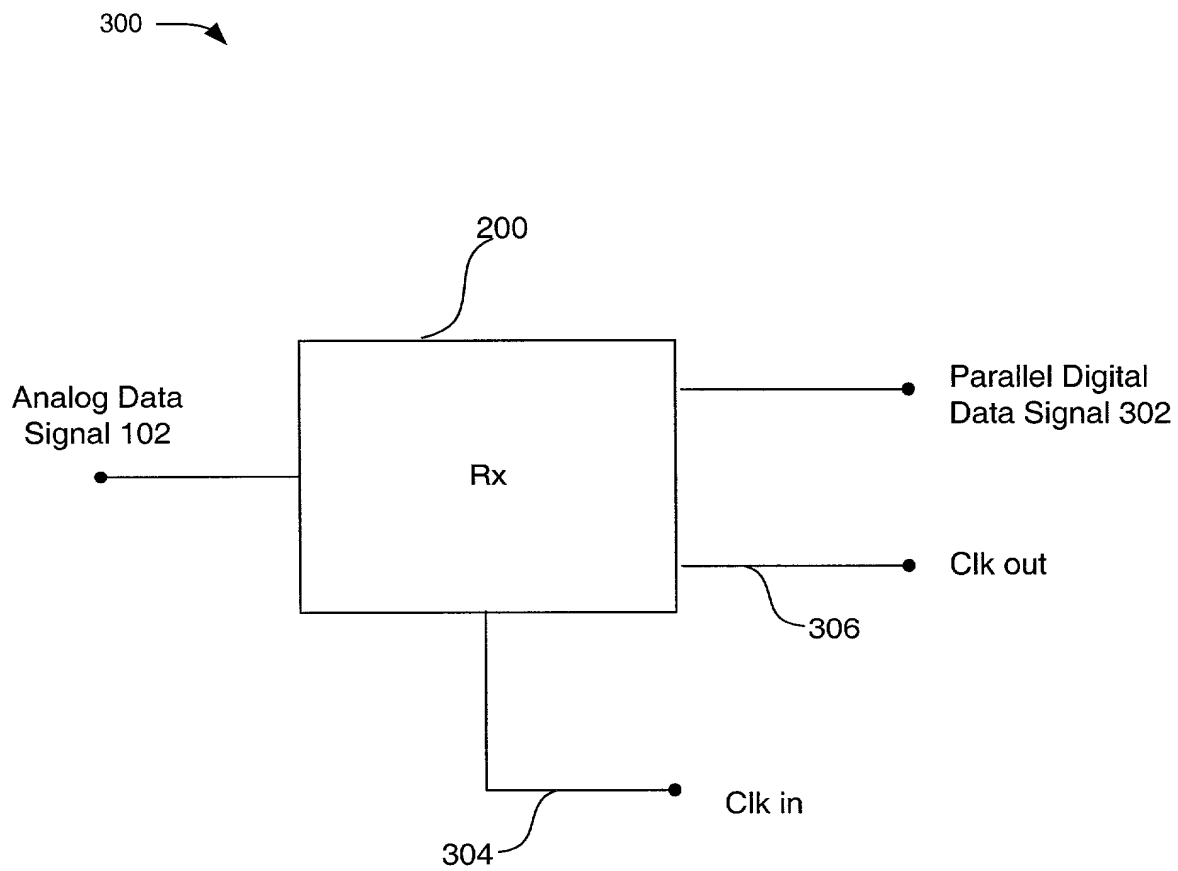


FIG. 2



Serial-to-Parallel Receiver

FIG. 3

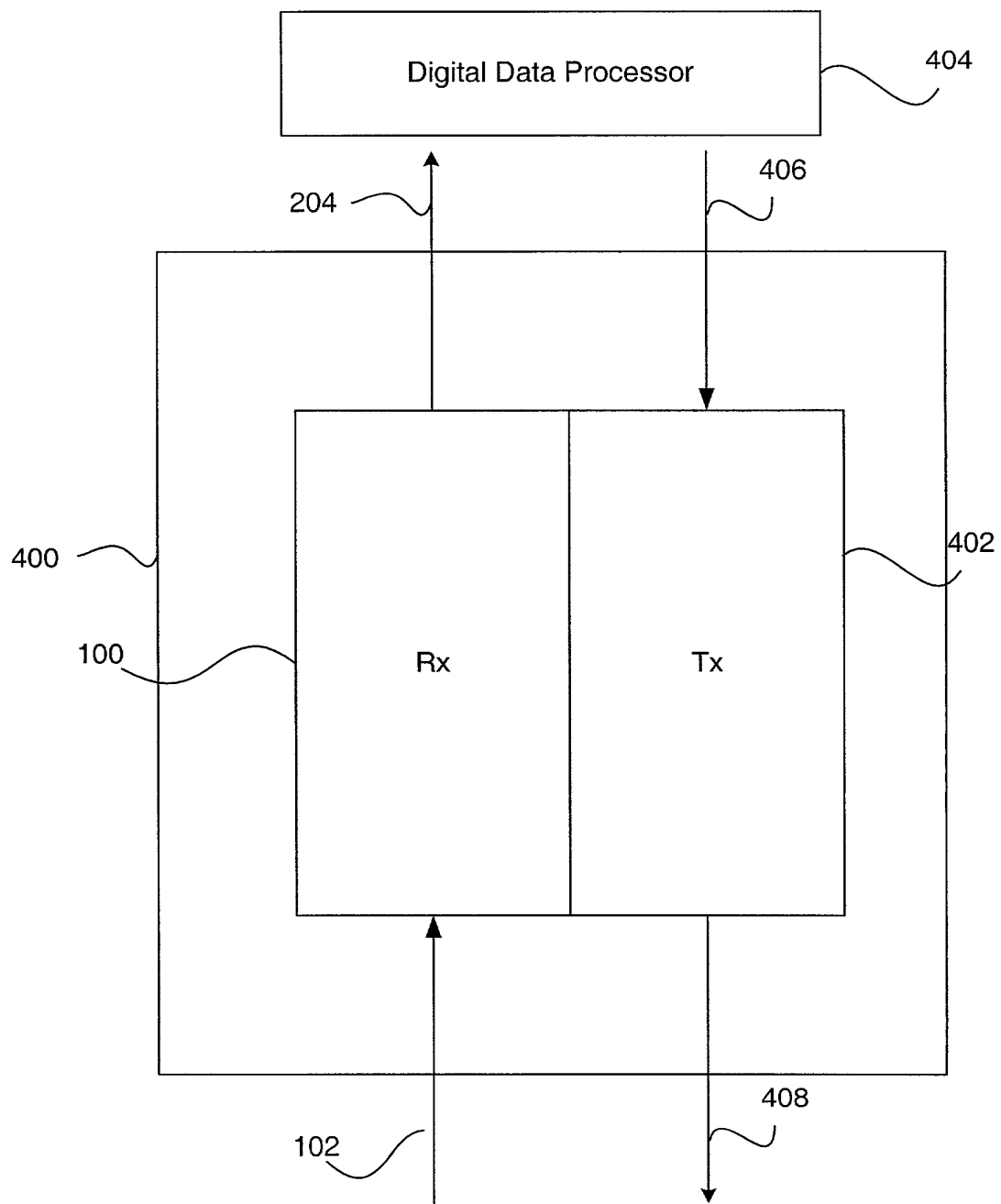


FIG. 4

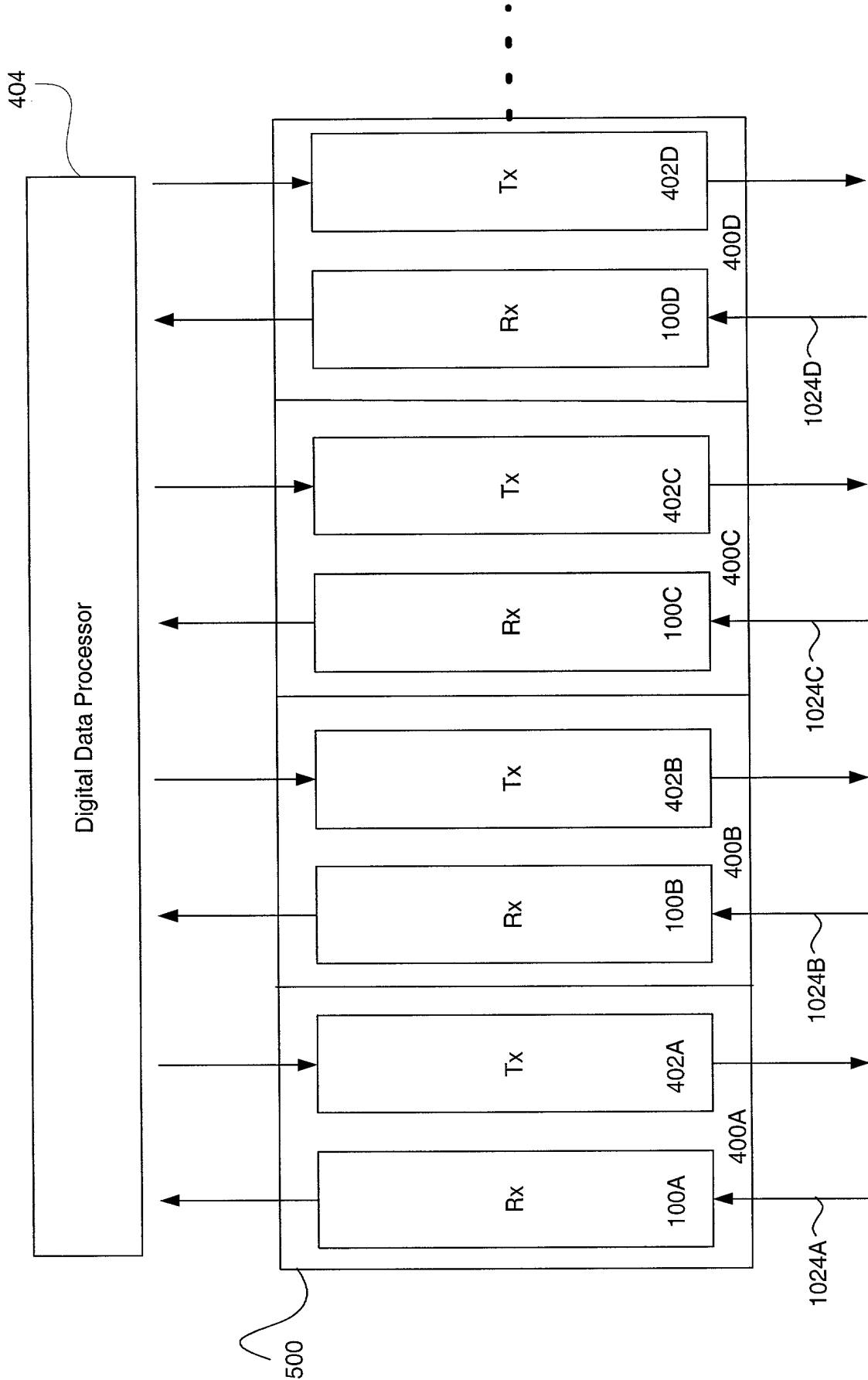


FIG. 5

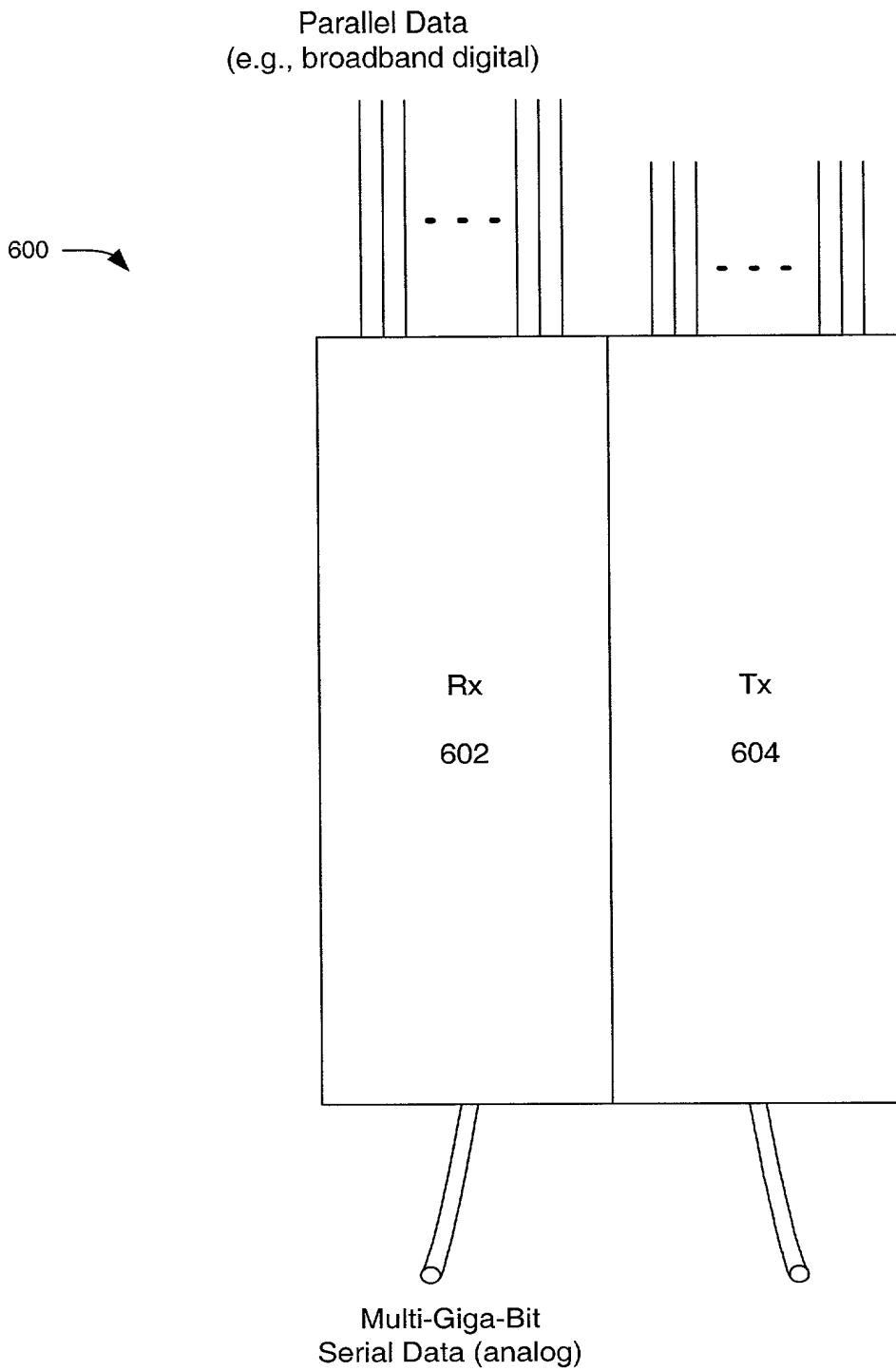


FIG. 6

700 →

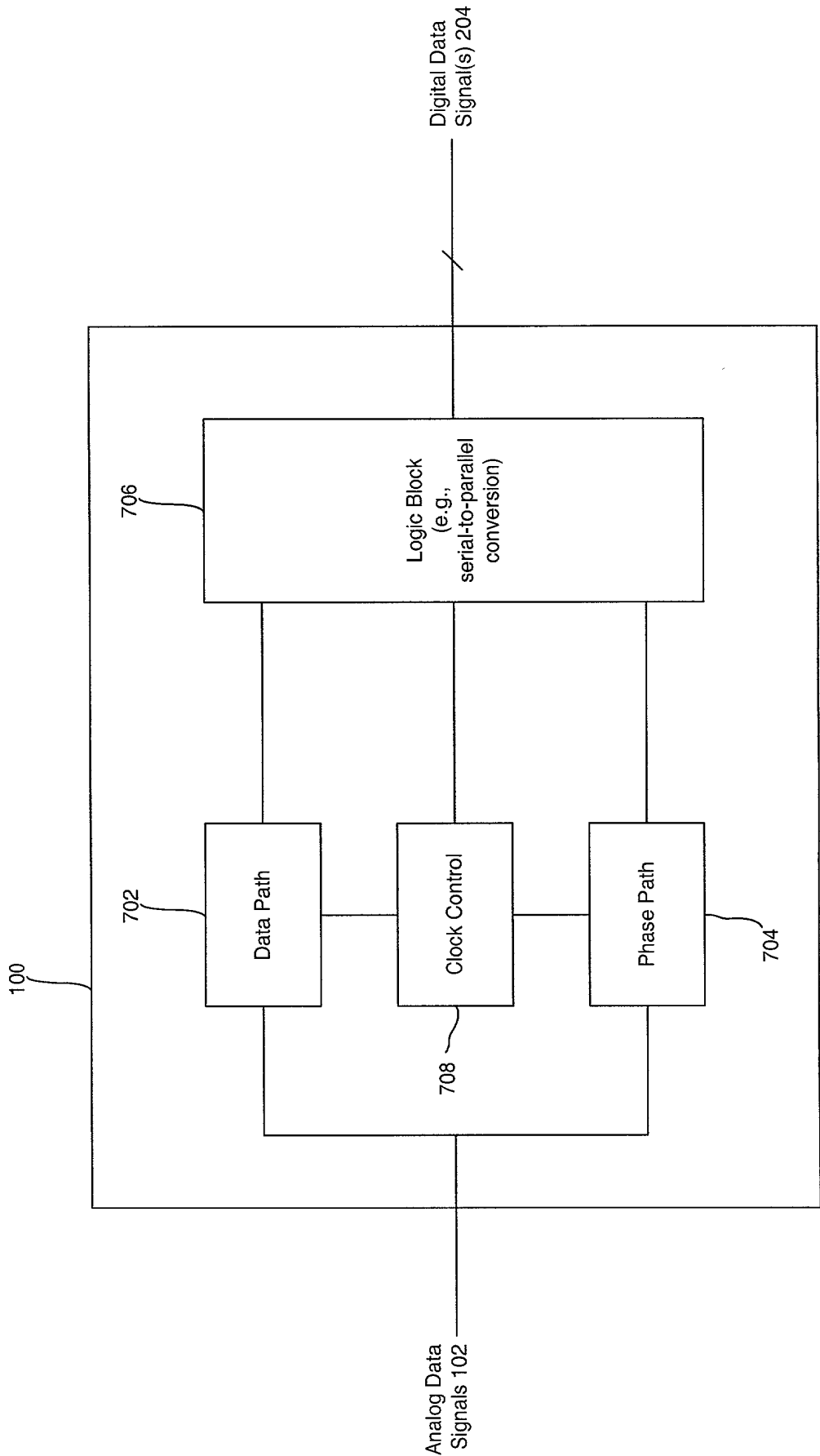


FIG. 7

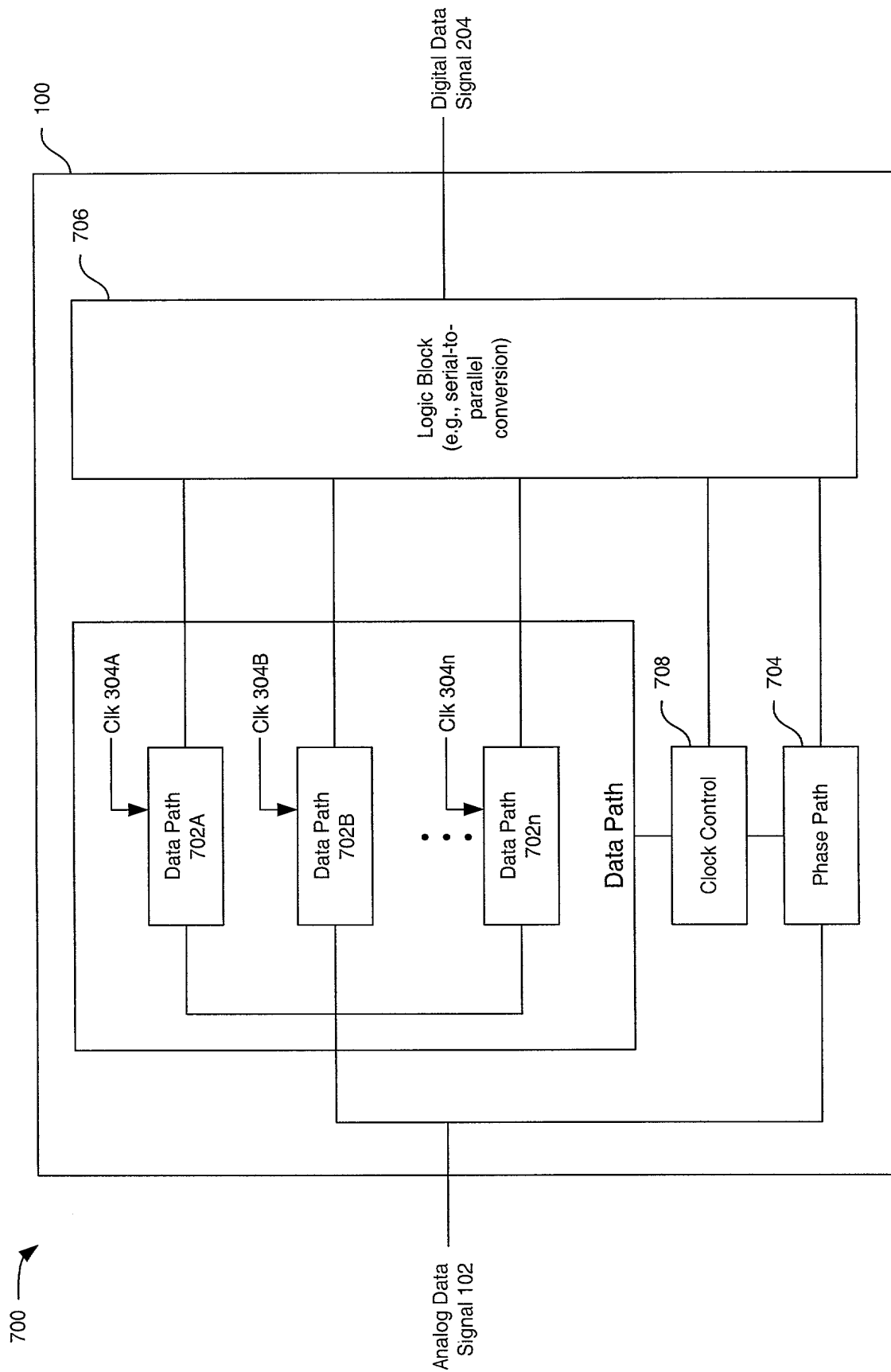


FIG. 8

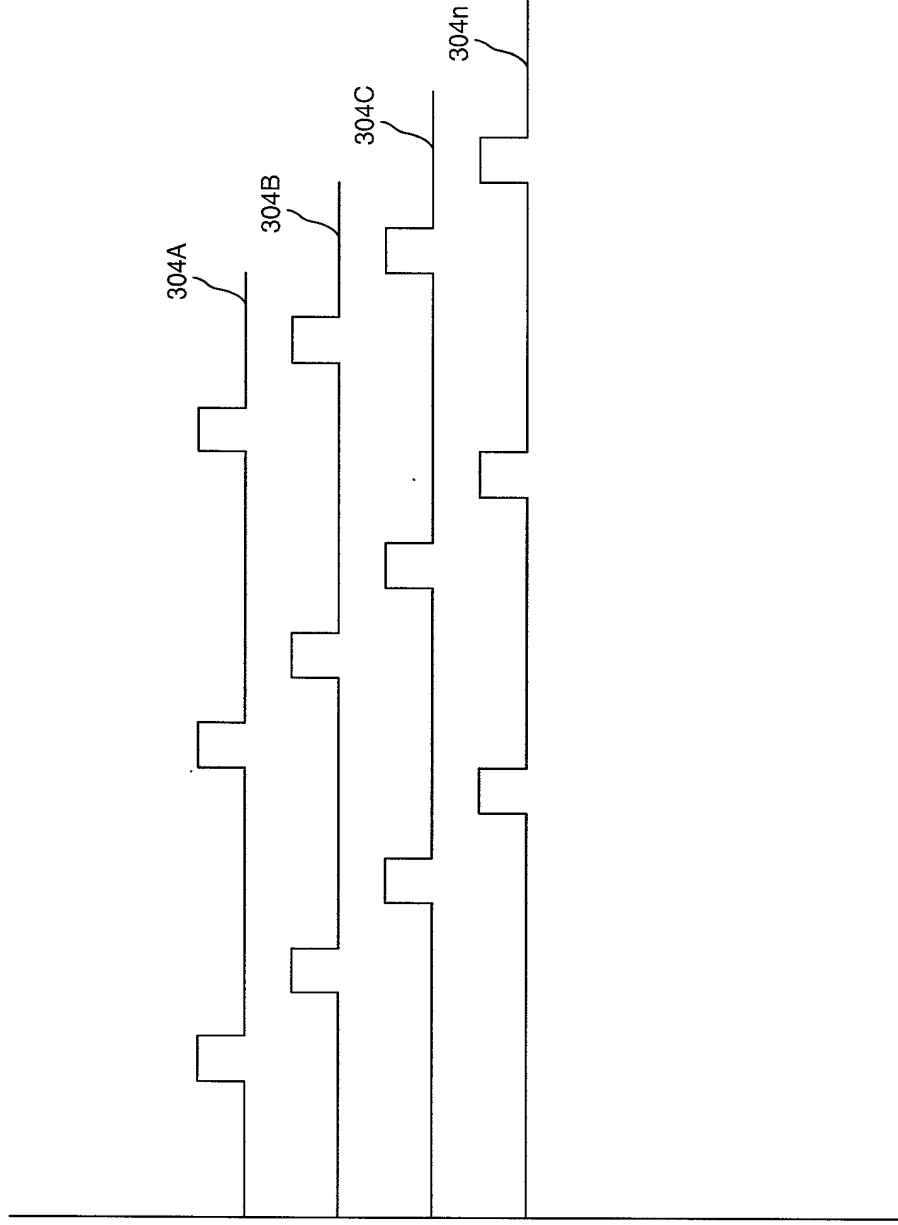


FIG. 9

Example Router

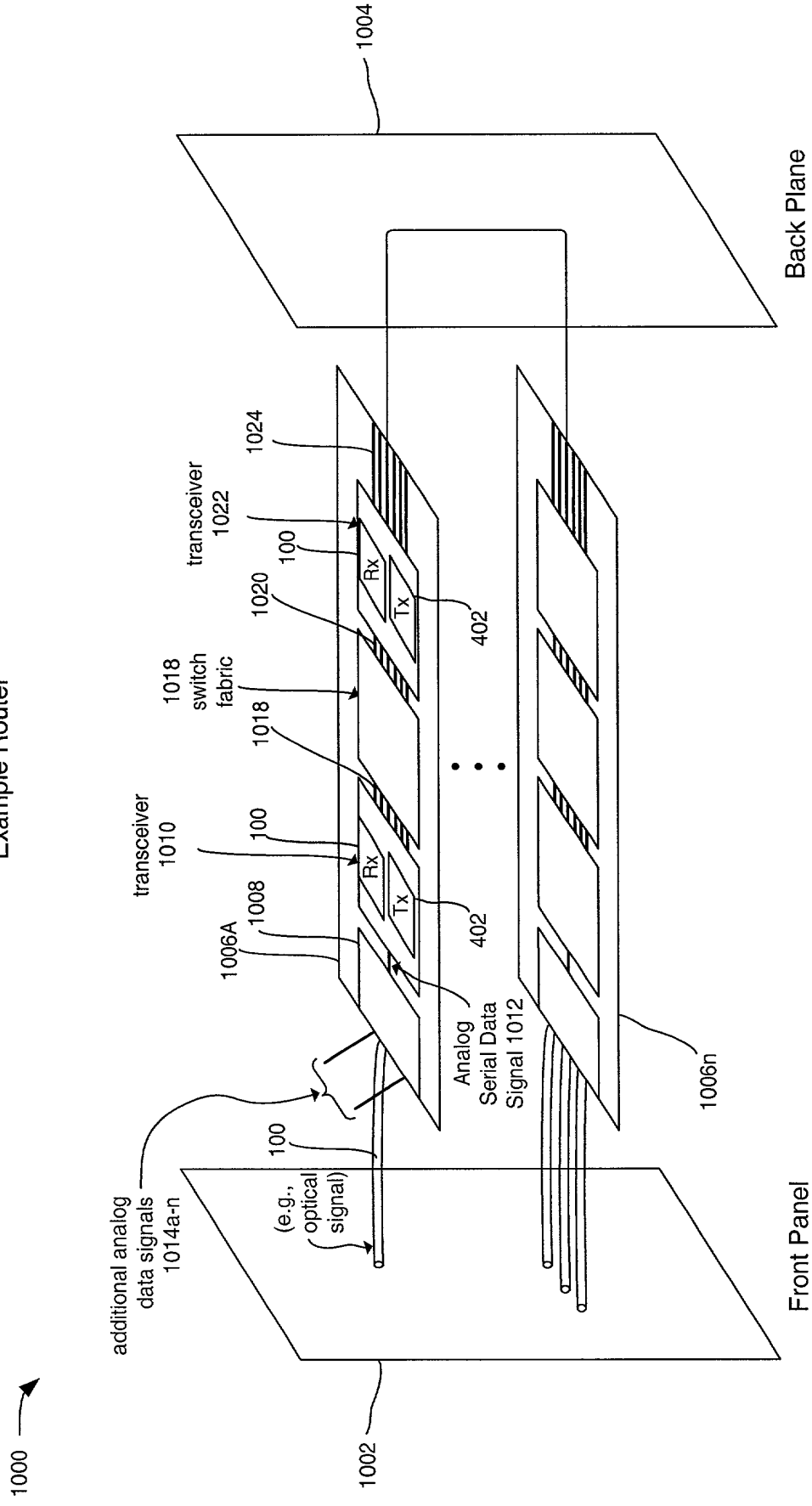


FIG. 10

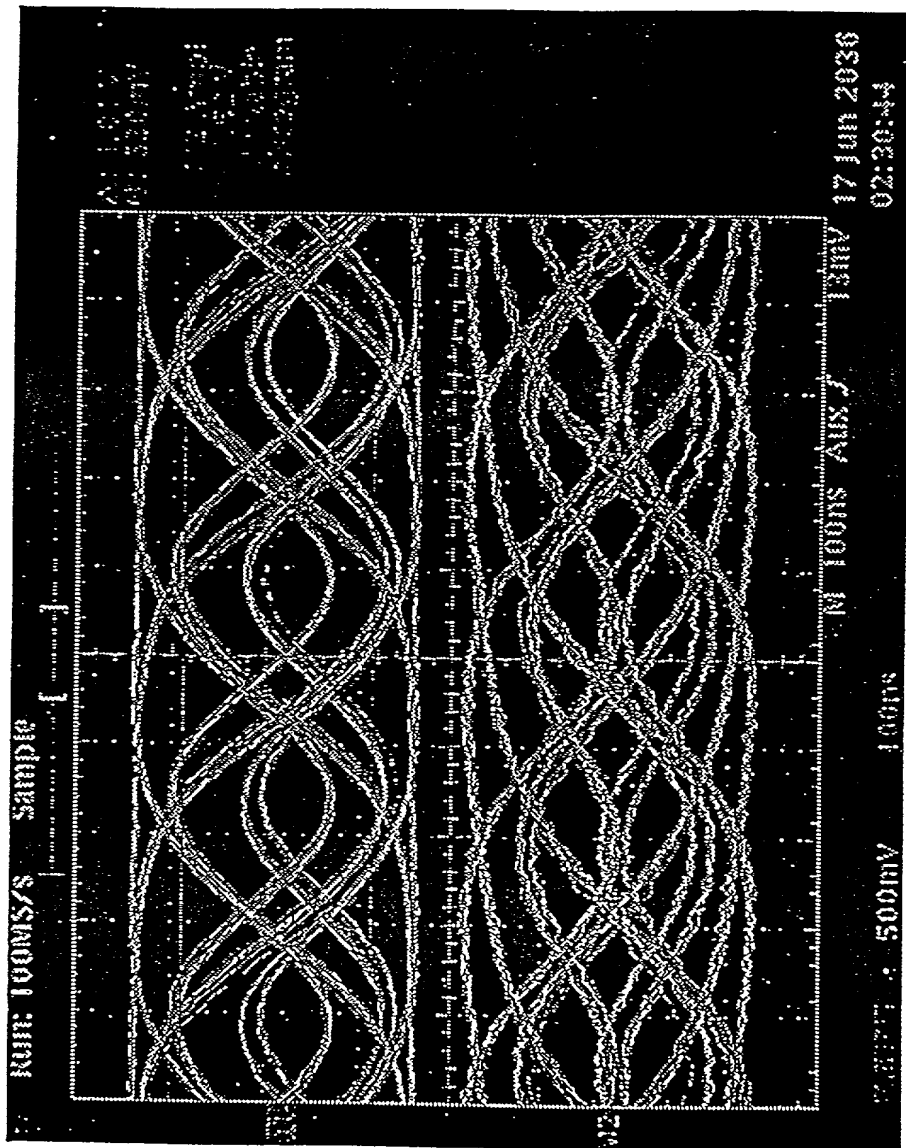
Receiver Eye Diagrams: 3.125-Gb/s

Backplane
36-inches FR4
No Equalization

FIG. 11A

Firewire
25-feet IEEE 1394
No Equalization

FIG. 11B



1200

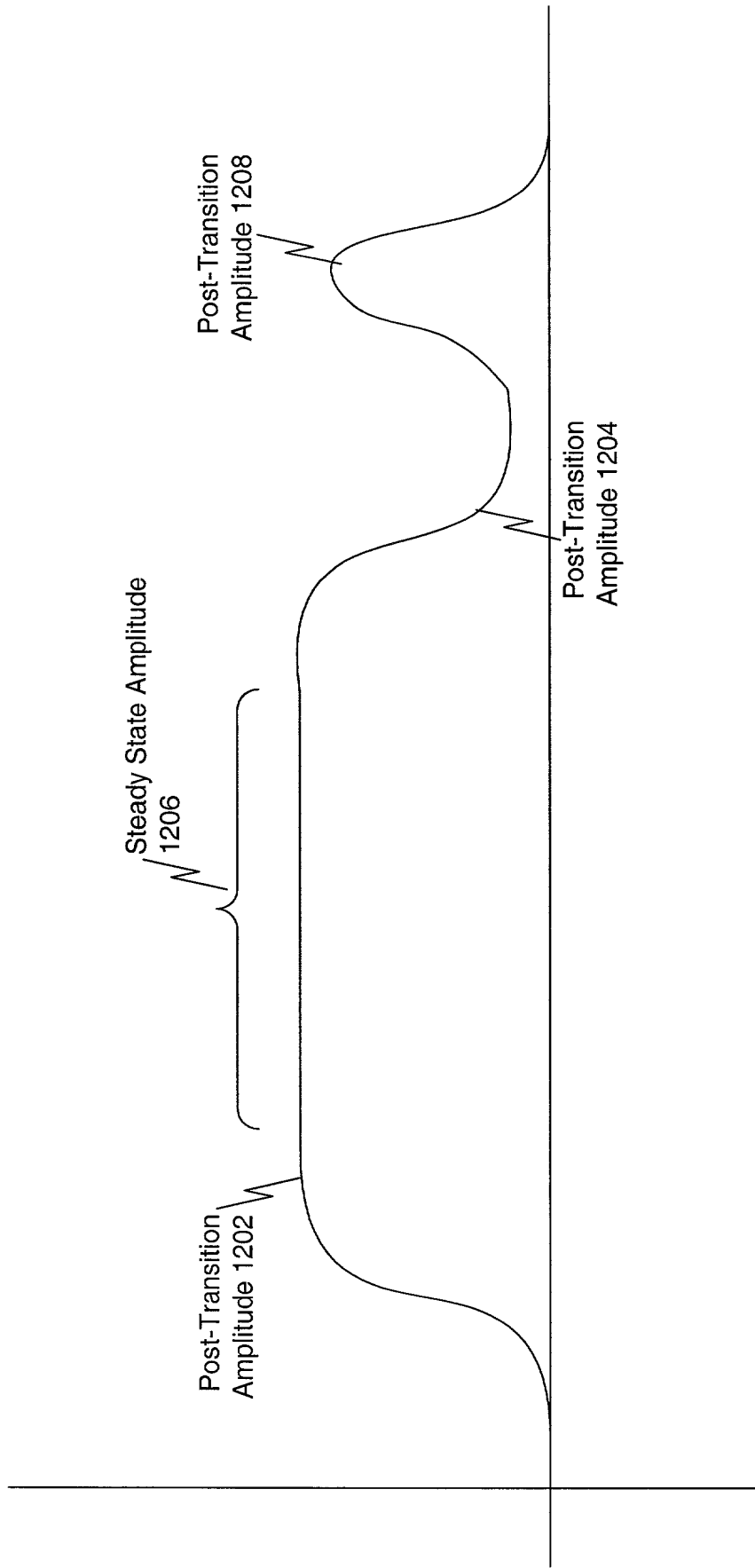


FIG. 12

Receiver Eye Diagrams: 3.125-Gb/s

Backplane
36-inches FR4
Equalization
 $a = 0.25$
Eye Opening = 900mV

Firewire
25-feet IEEE 1394
Equalization
 $a = 0.375$
Eye Opening = 750mV

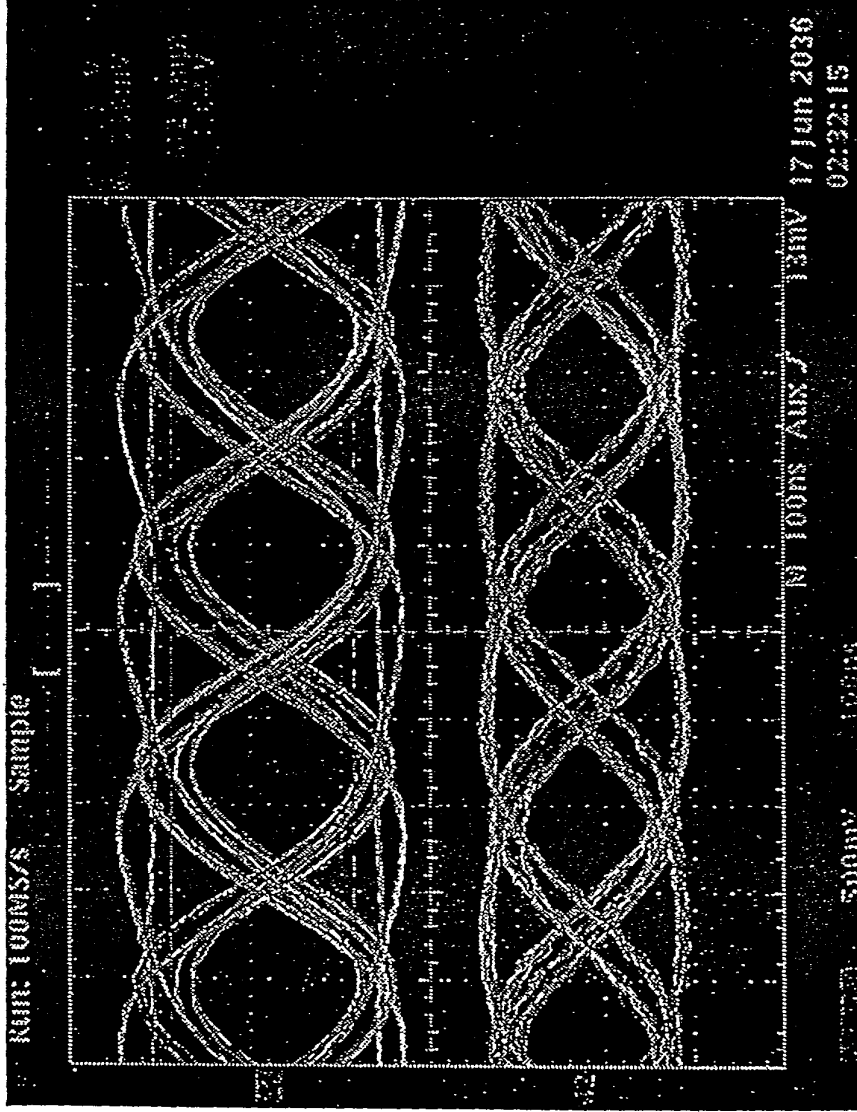


FIG. 13A

FIG. 13B

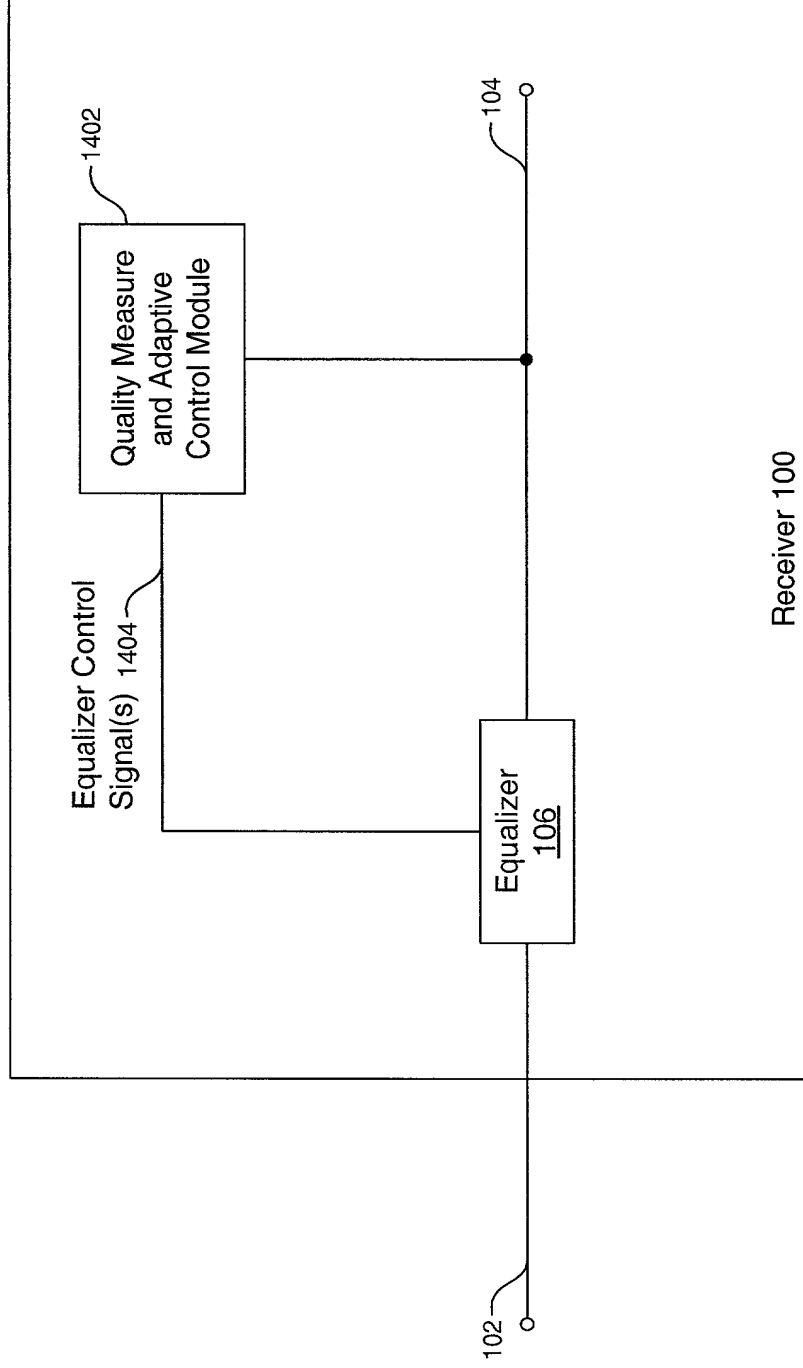


FIG. 14A

200 →

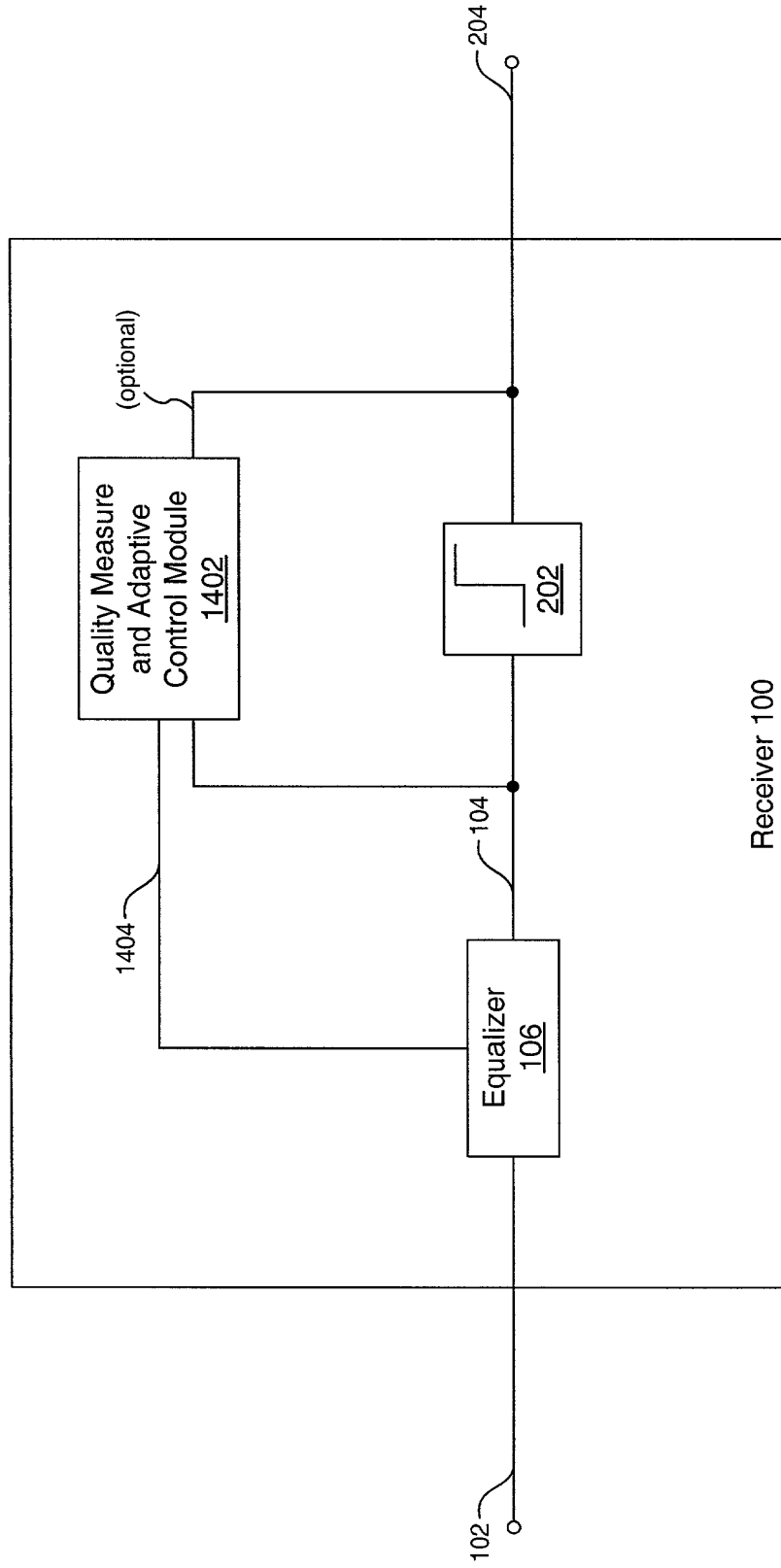


FIG. 14B

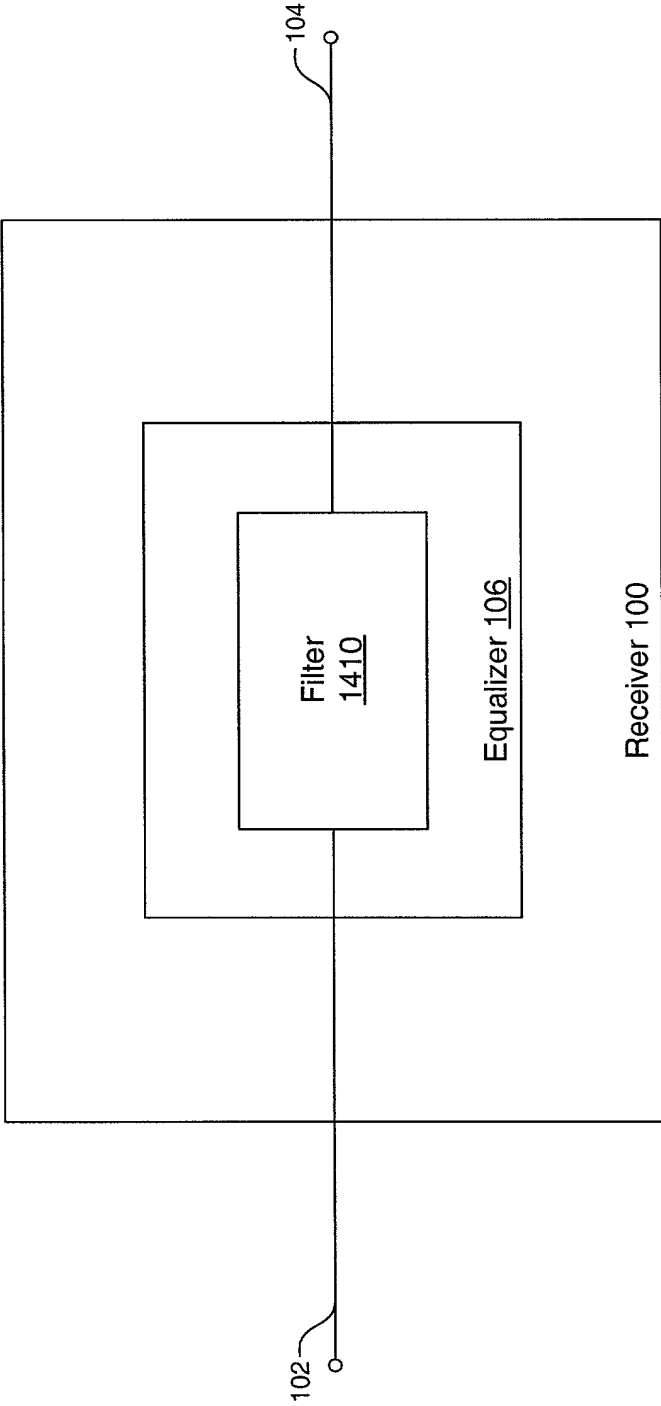


FIG. 14C

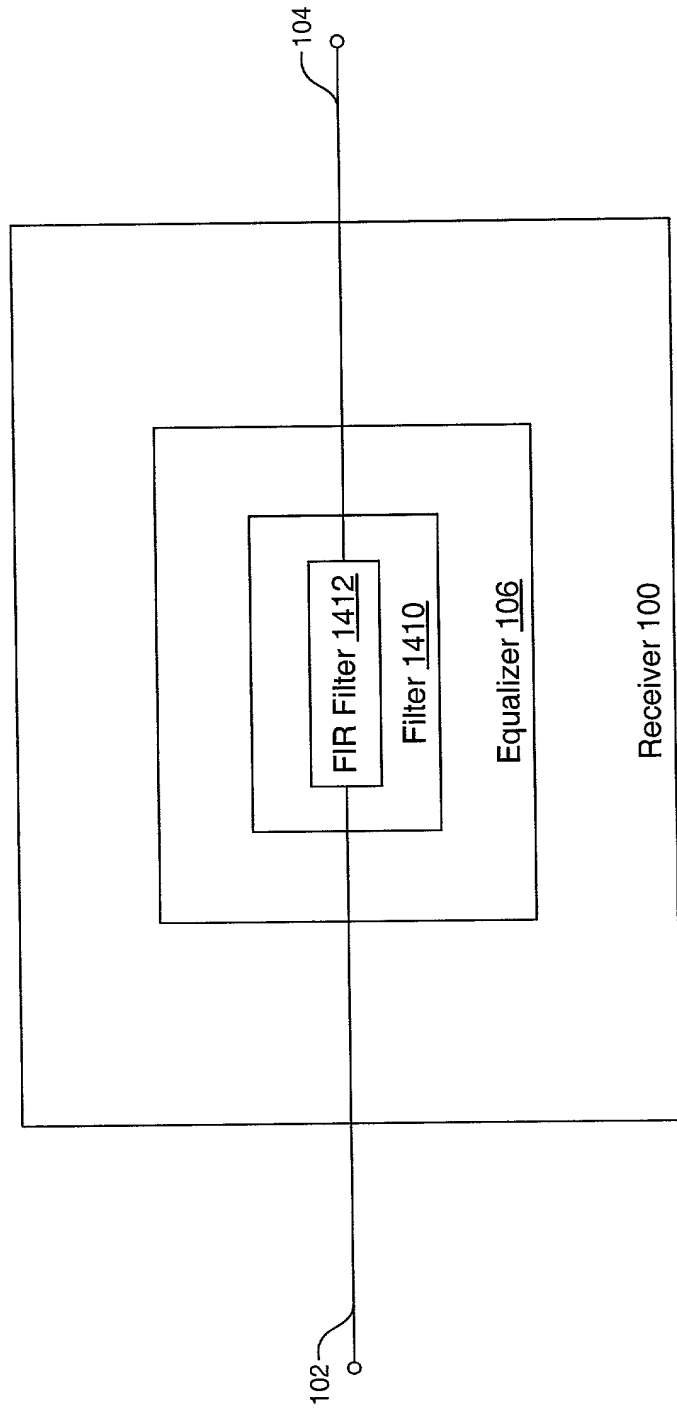


FIG. 14D

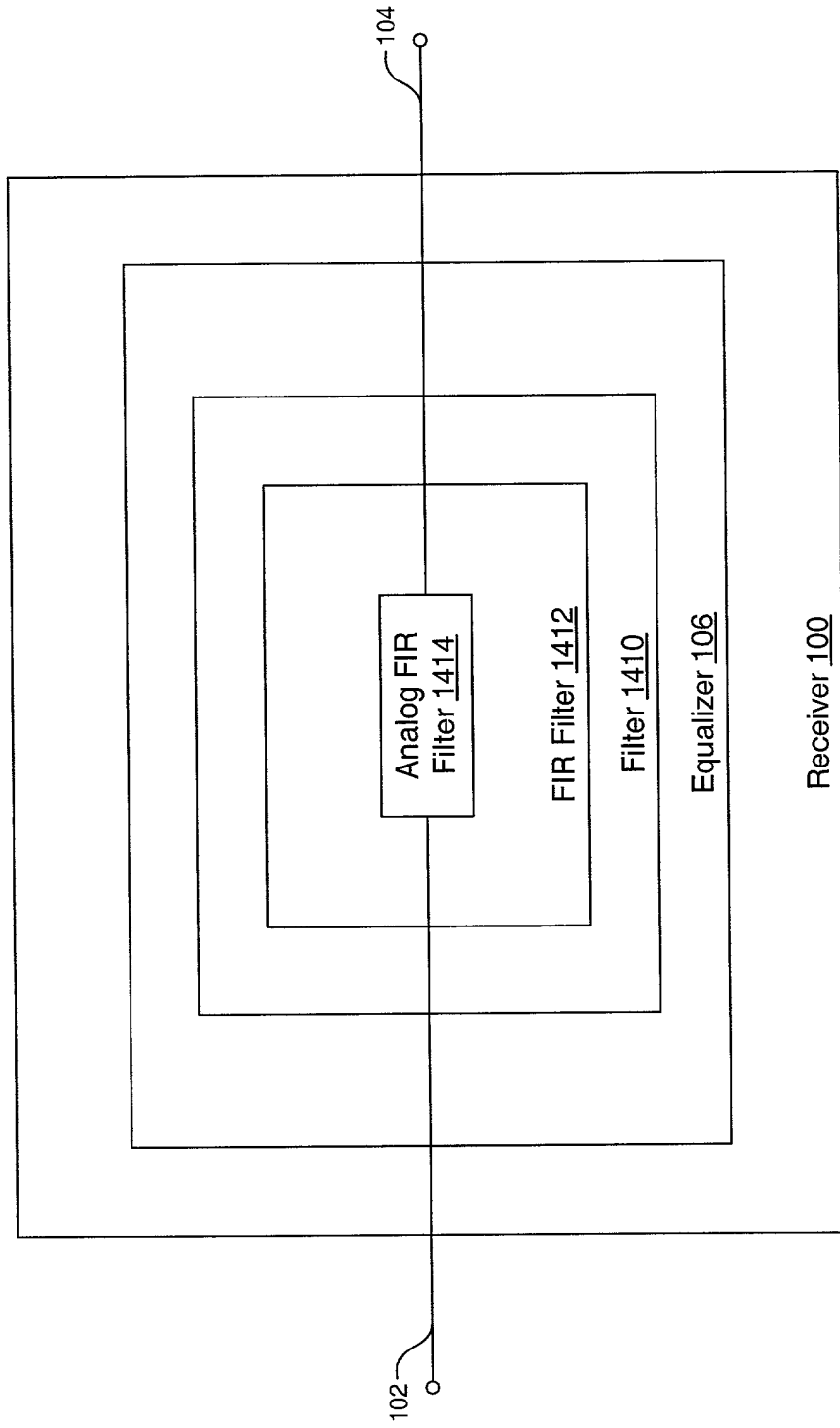


FIG. 14E

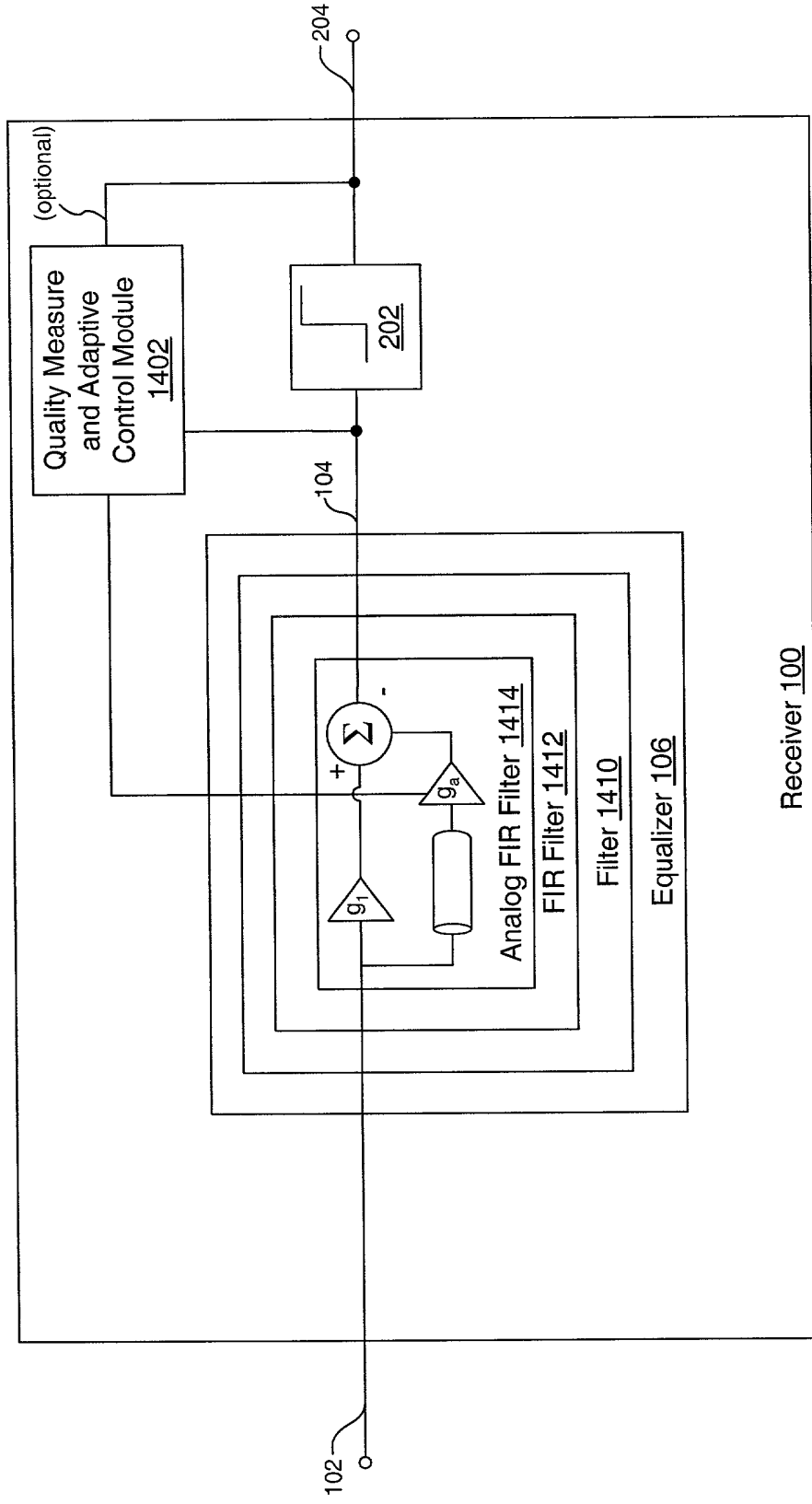


FIG. 14F

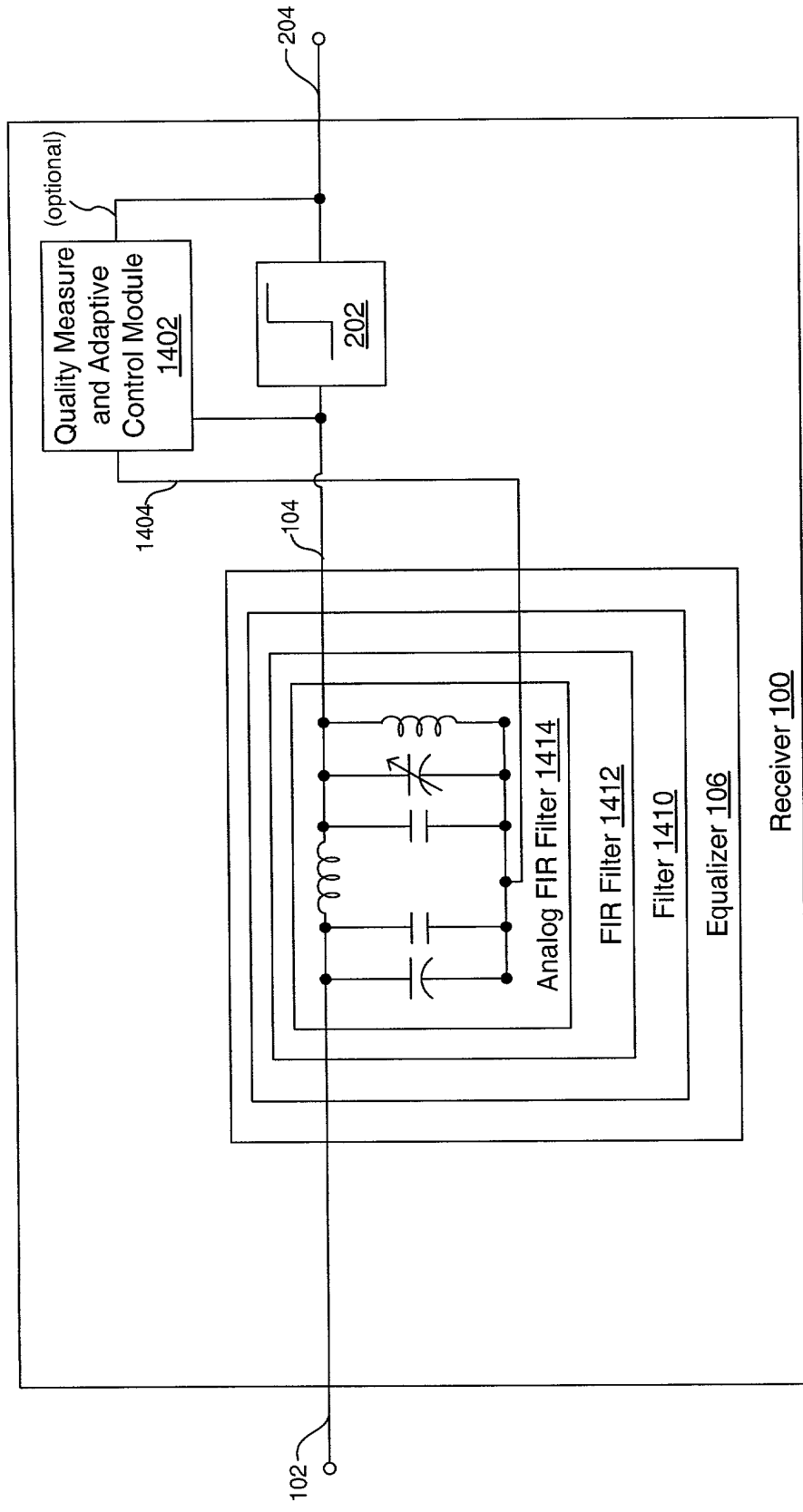


FIG. 14G

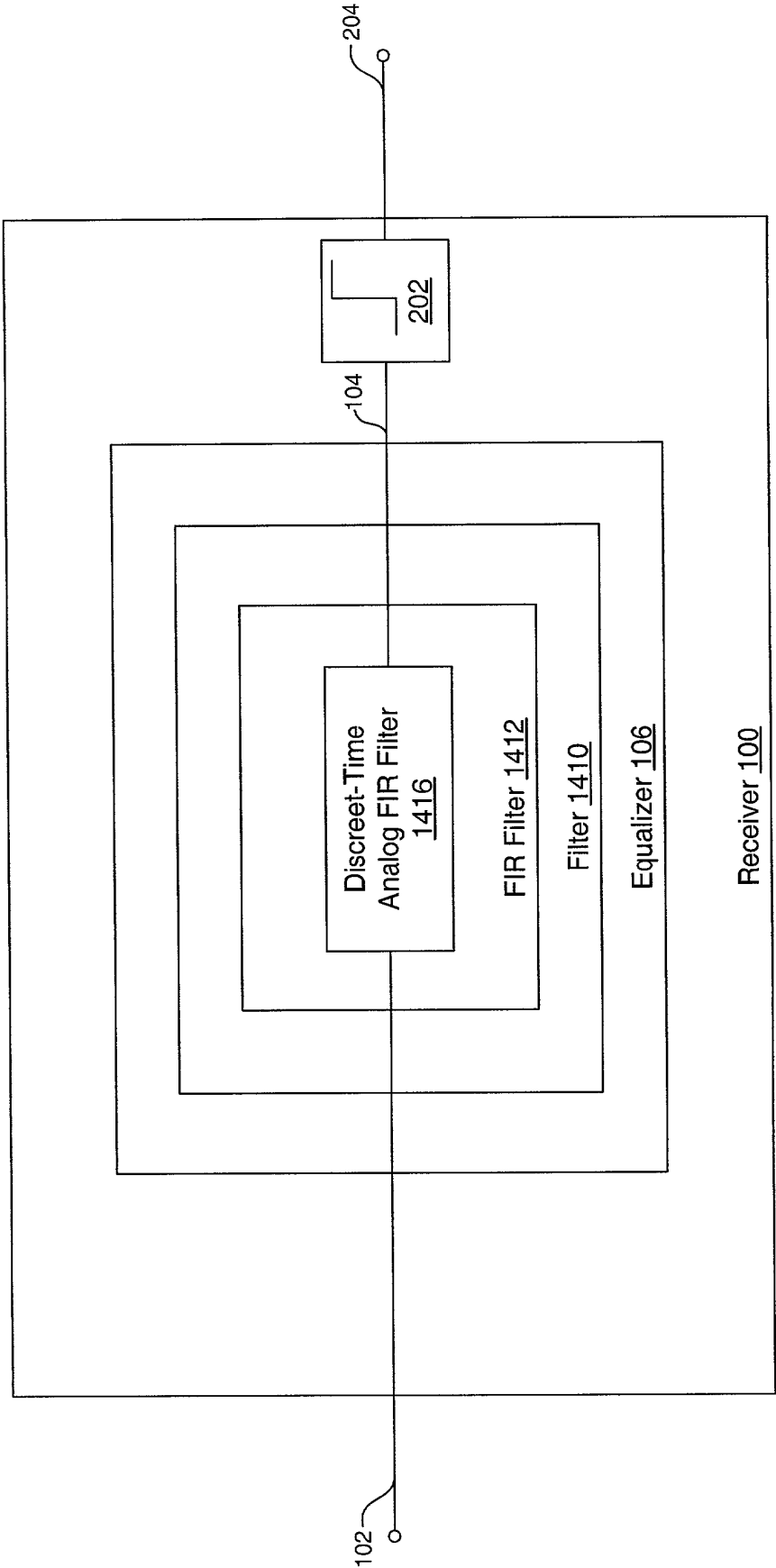


FIG. 14H

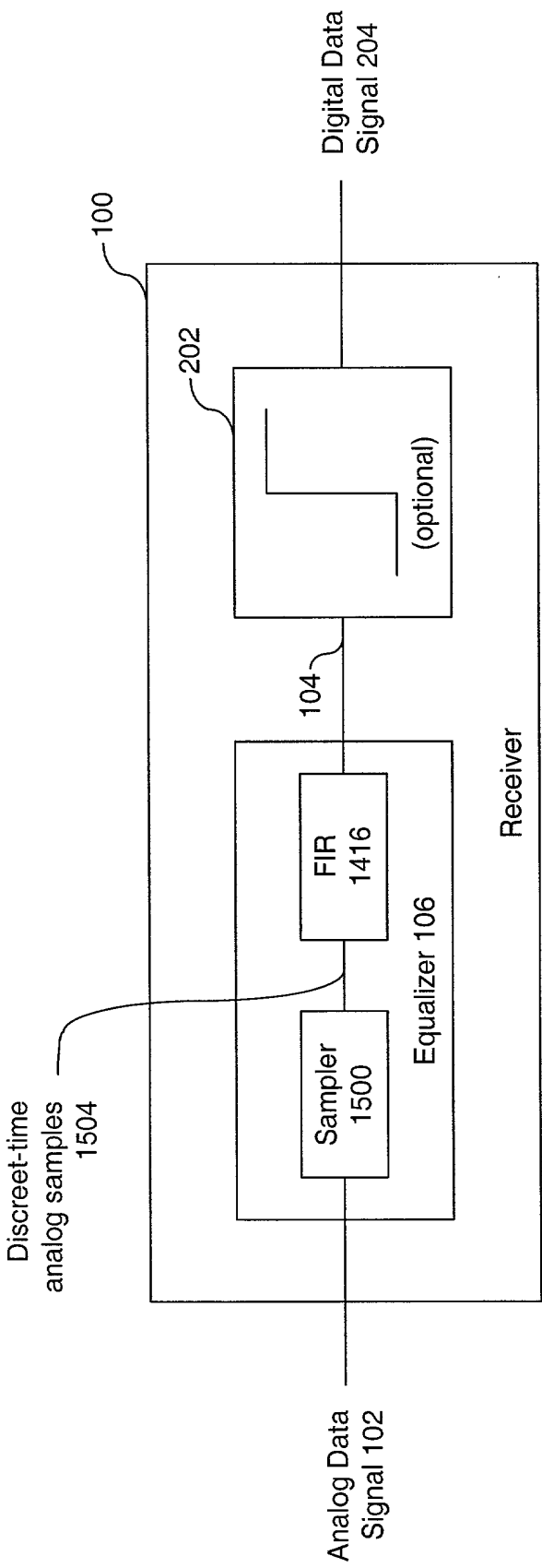


FIG. 15

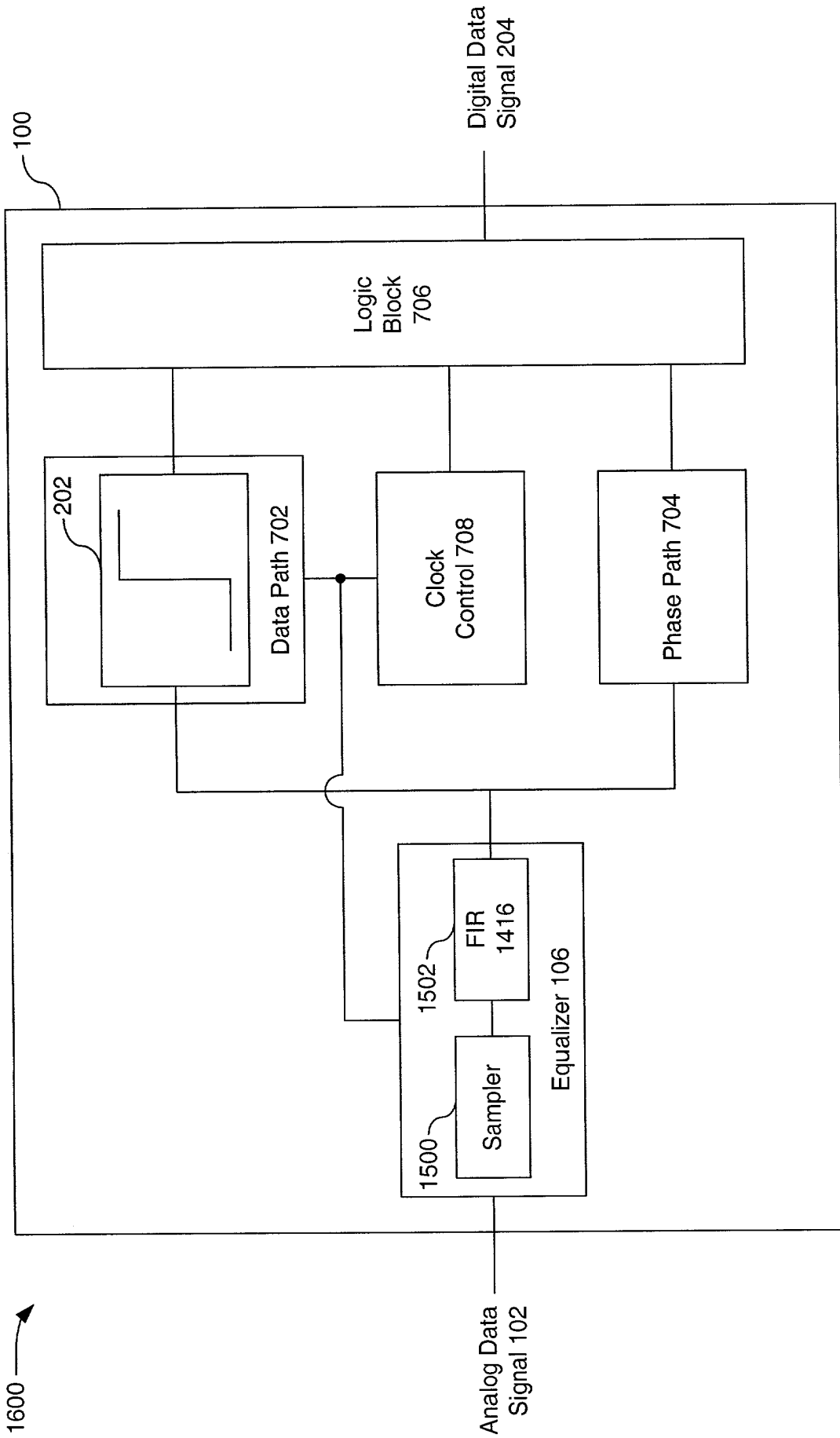


FIG. 16

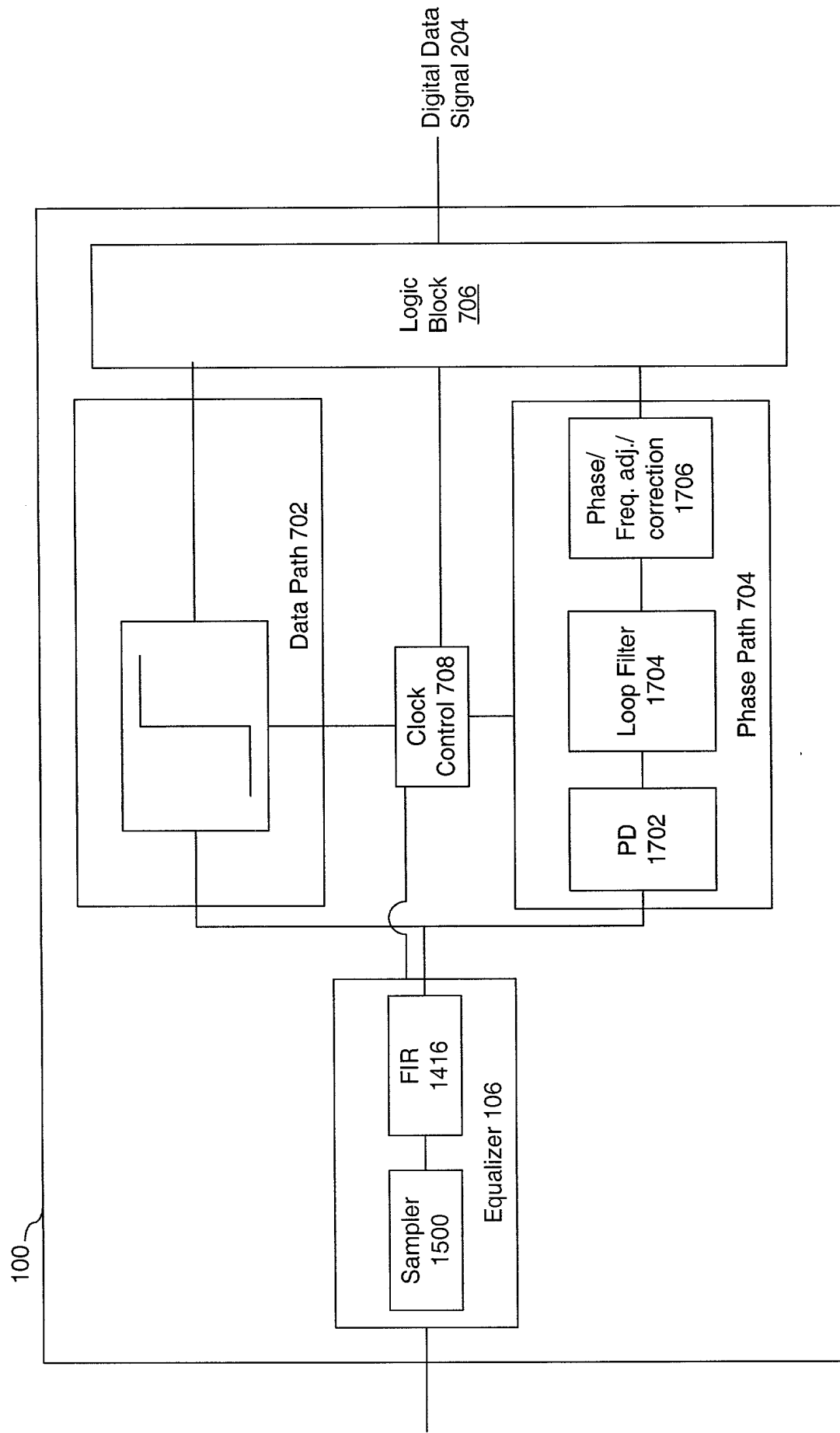


FIG. 17

1800 →

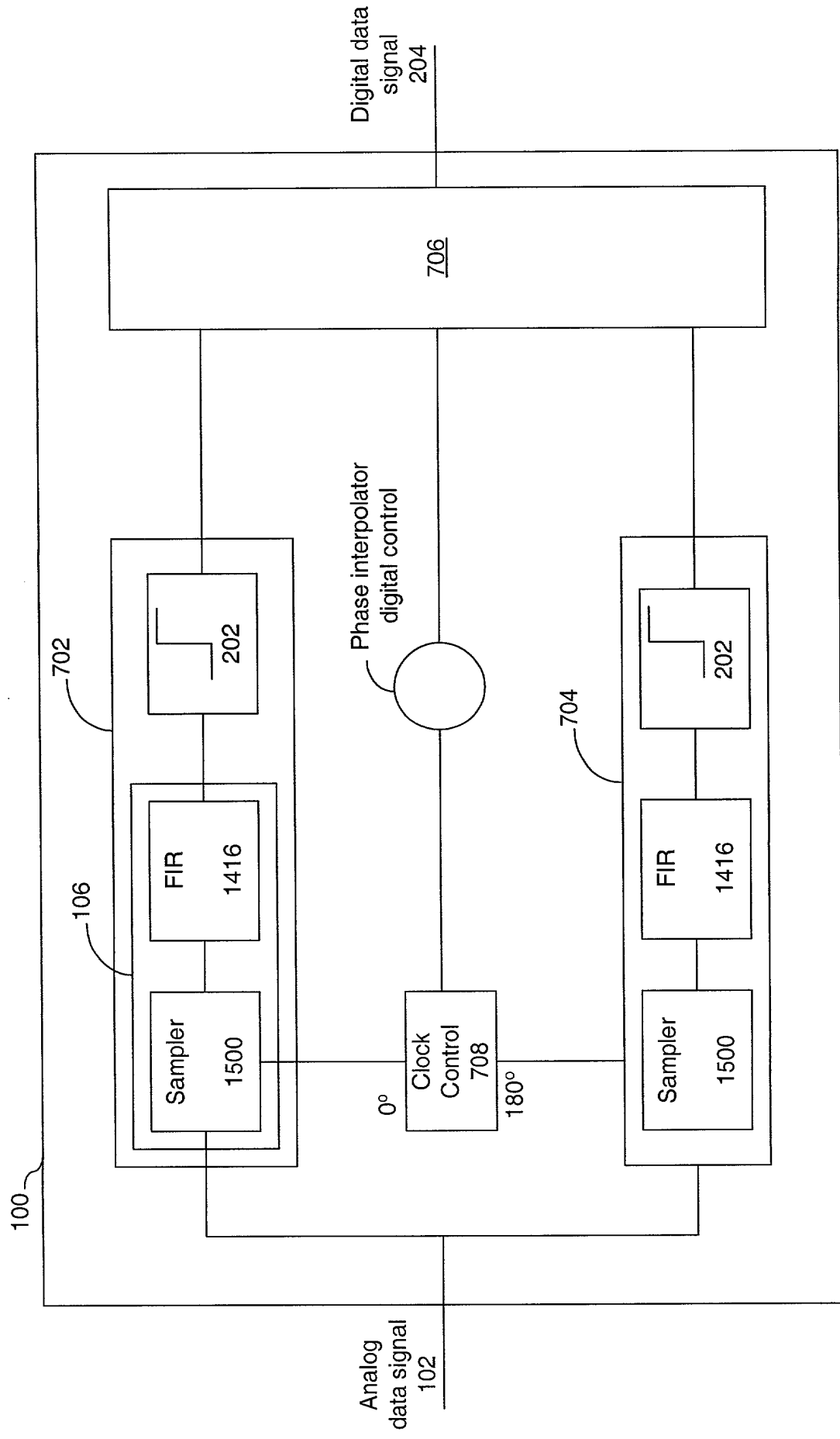


FIG. 18

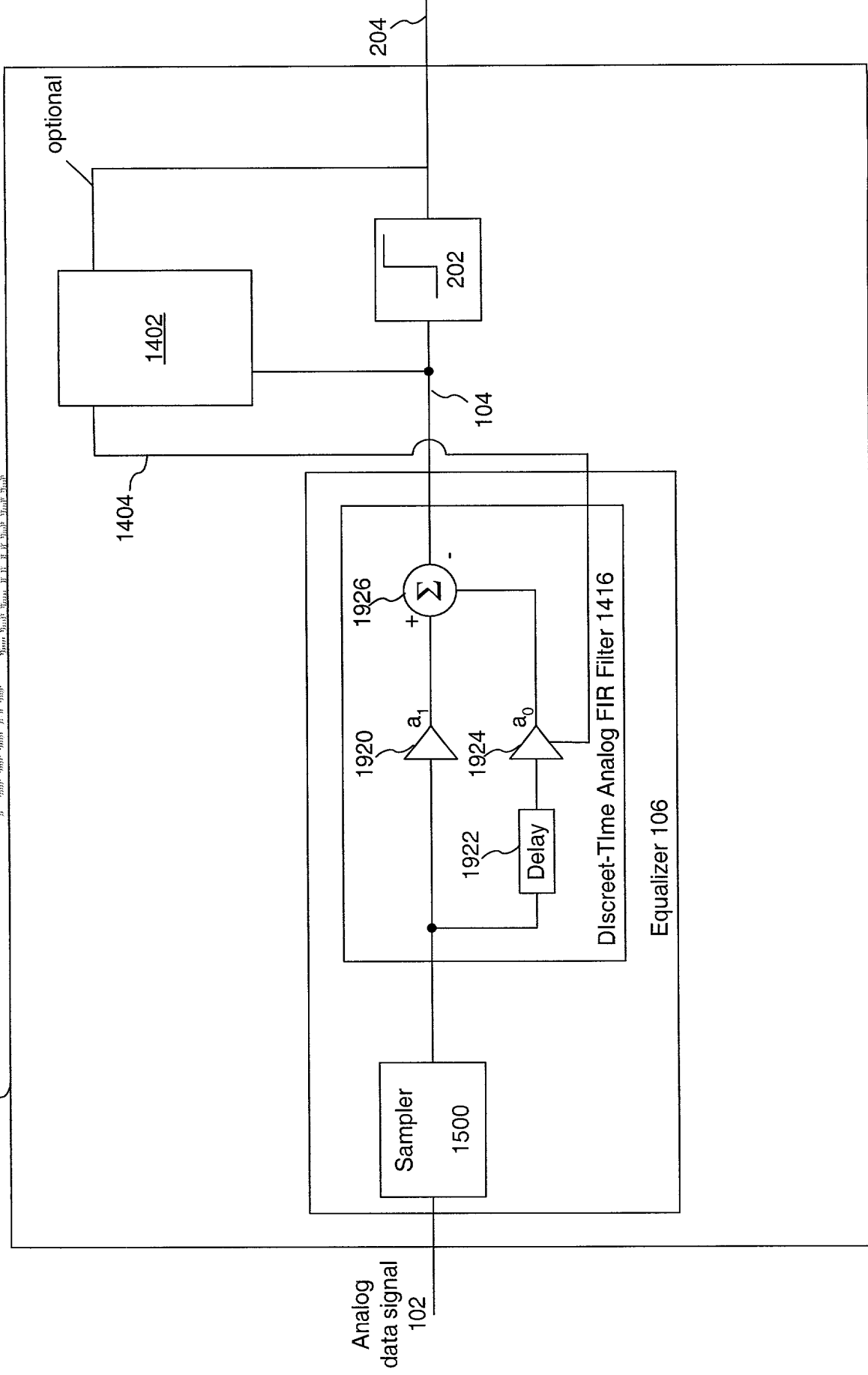


FIG. 19

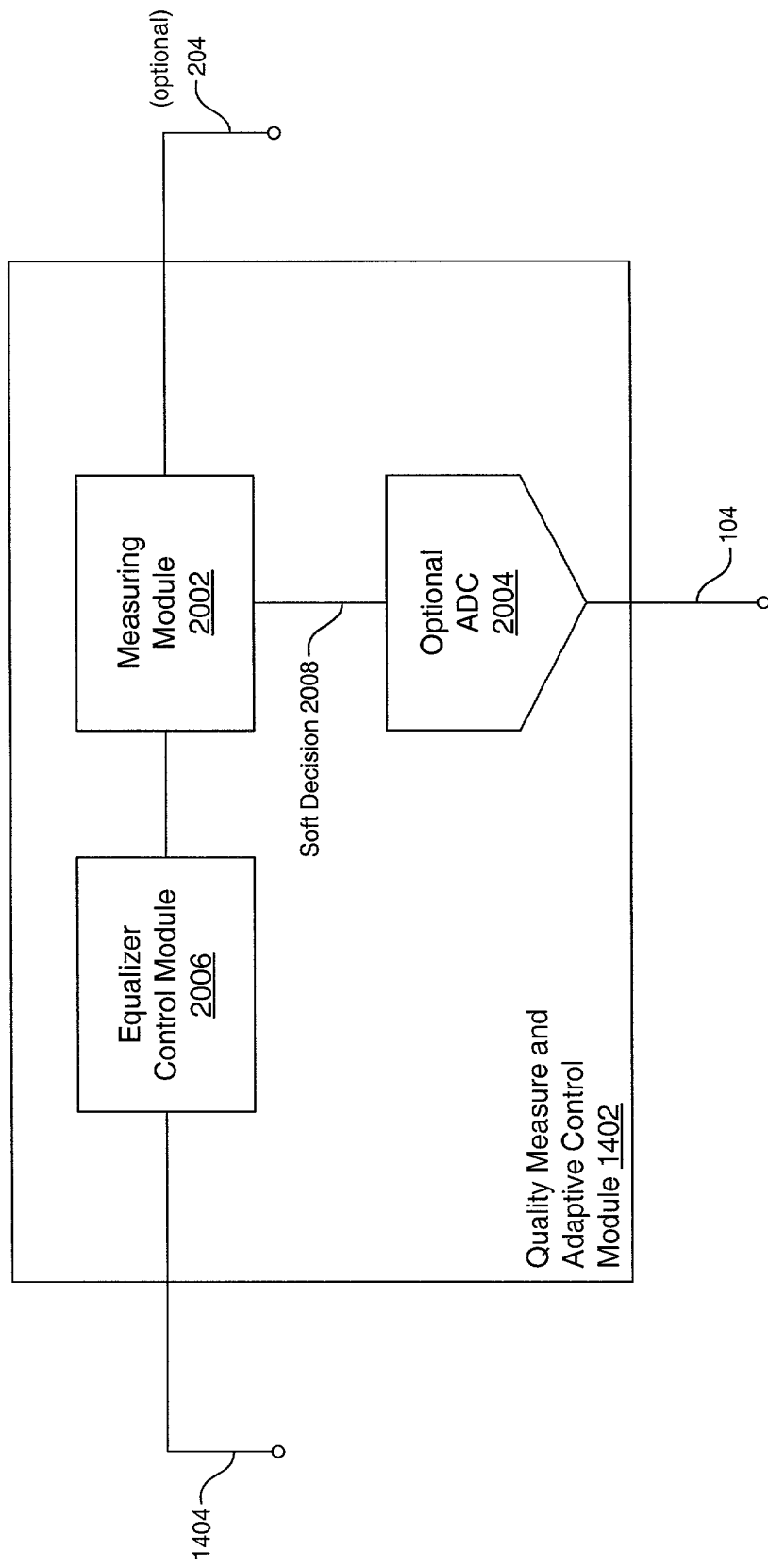


FIG. 20

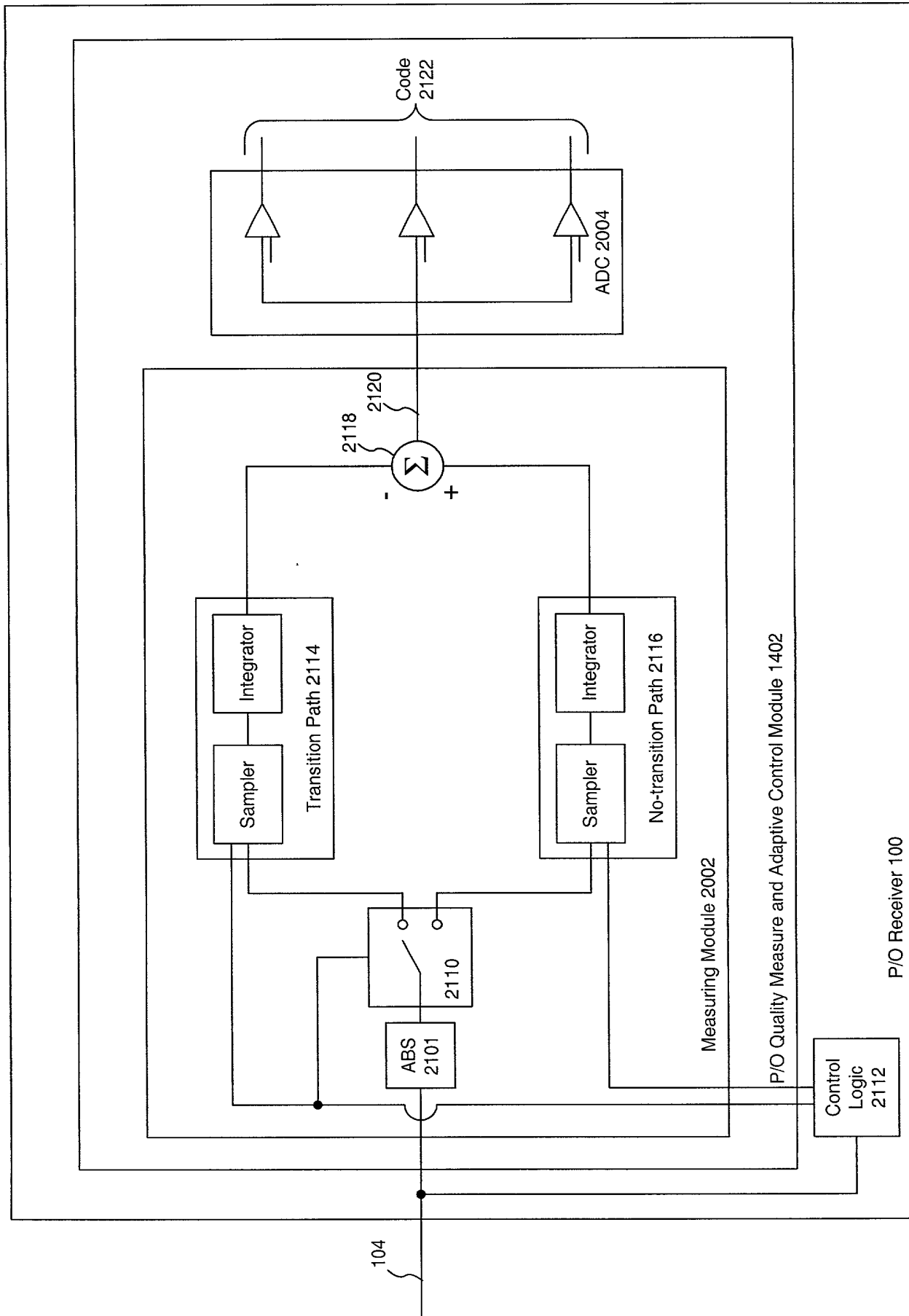


FIG. 21A

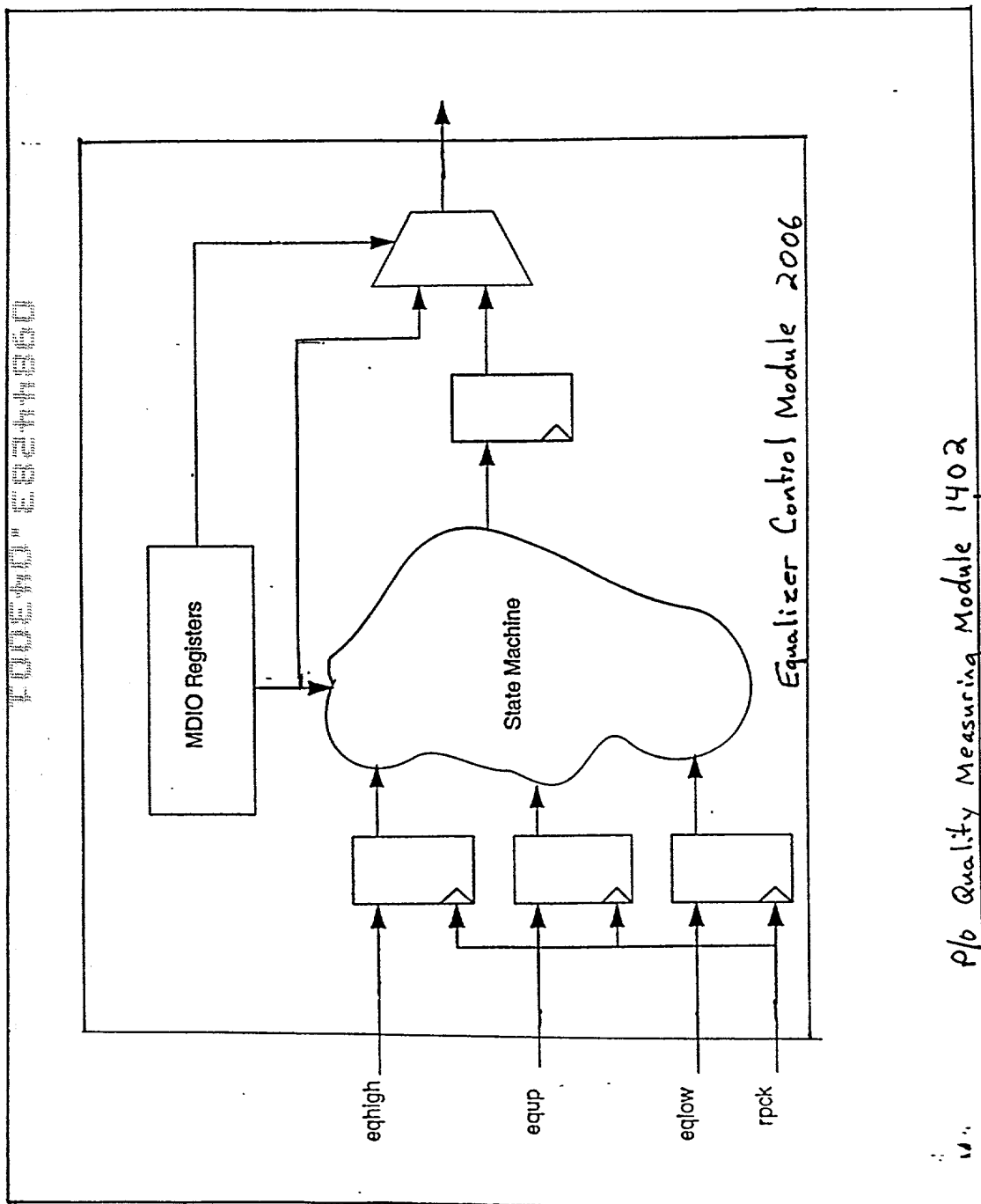


FIG. 21B

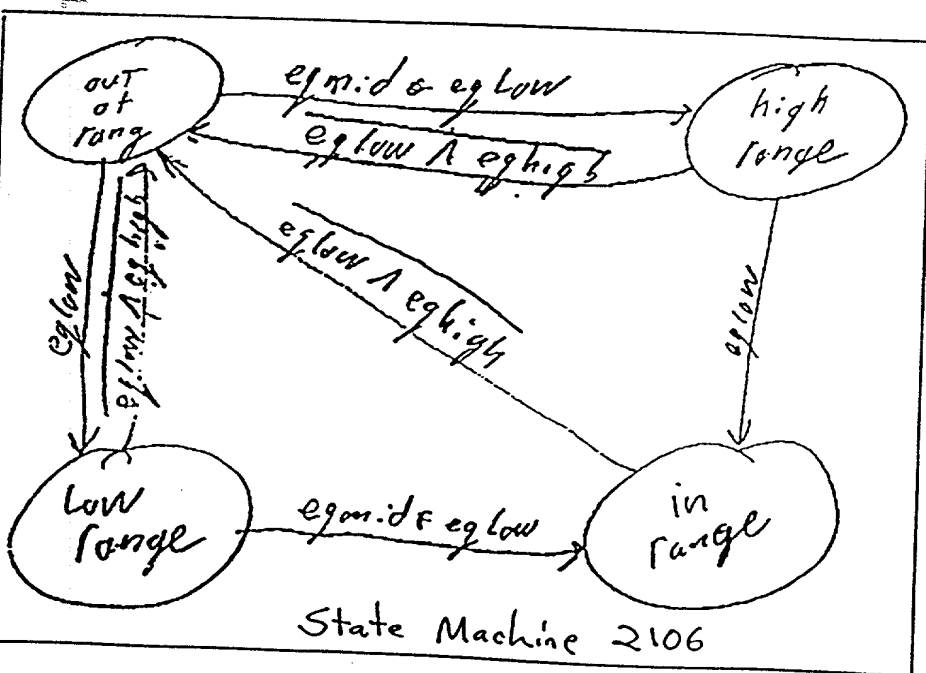
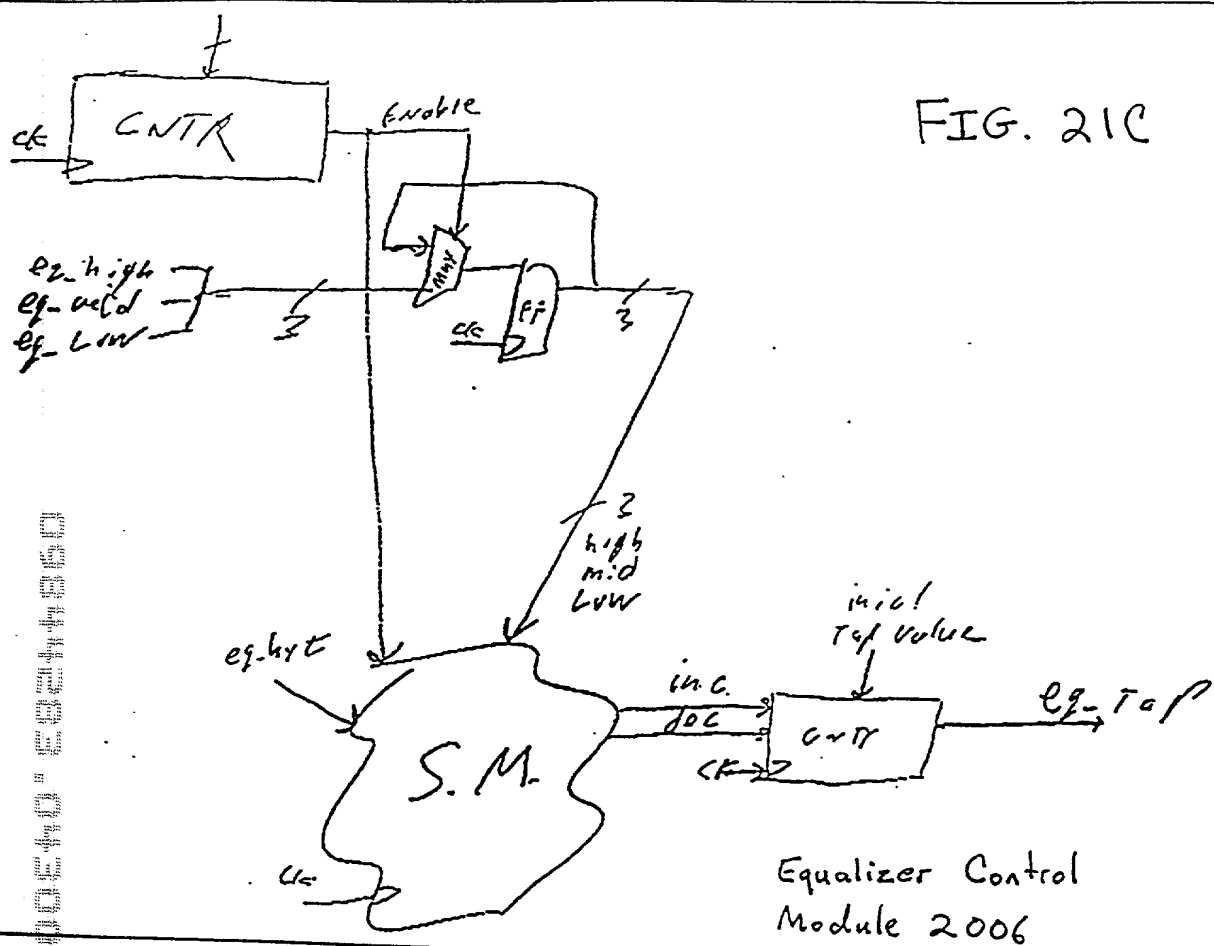


FIG. 21D

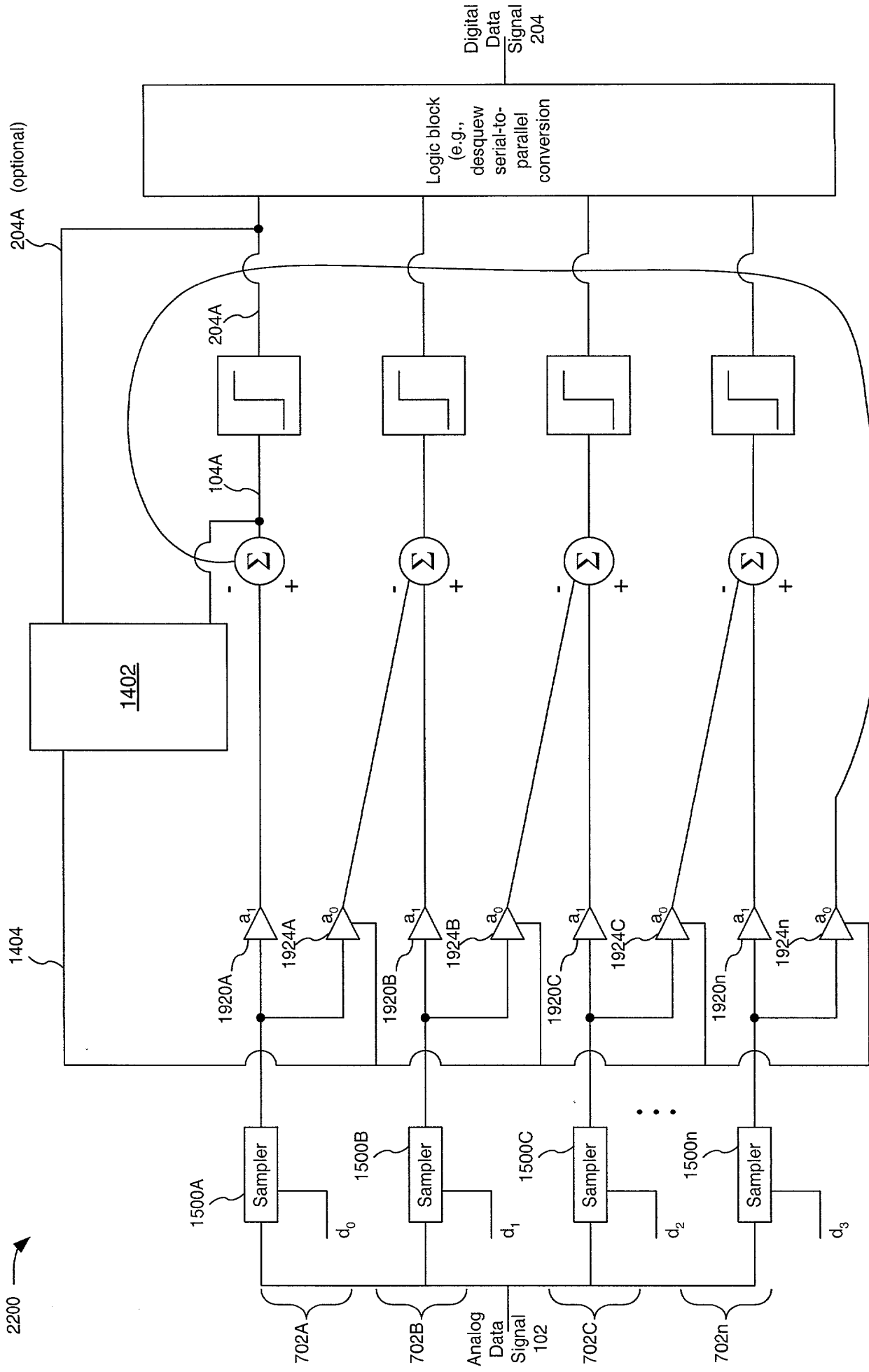


FIG. 22

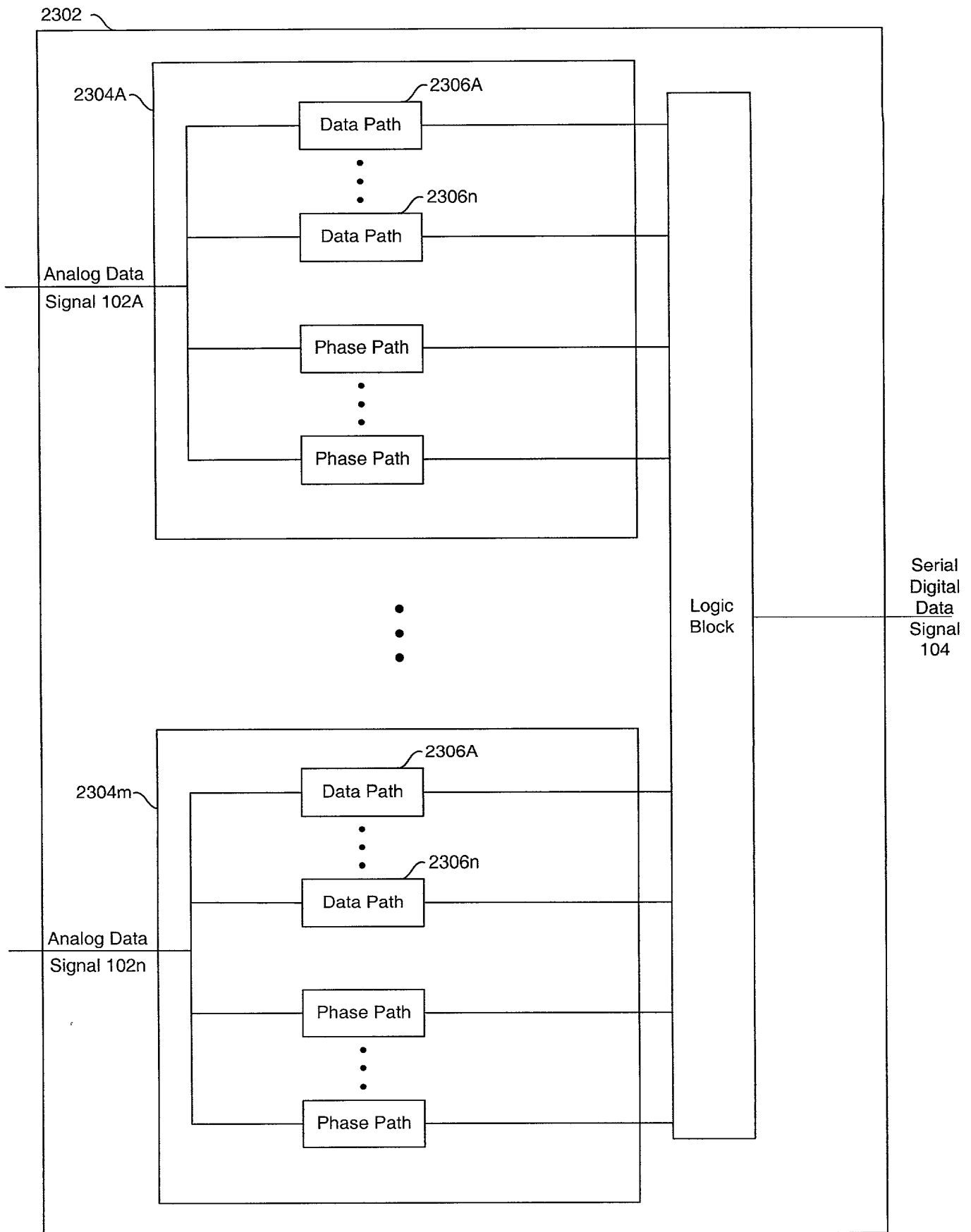


FIG. 23

2400

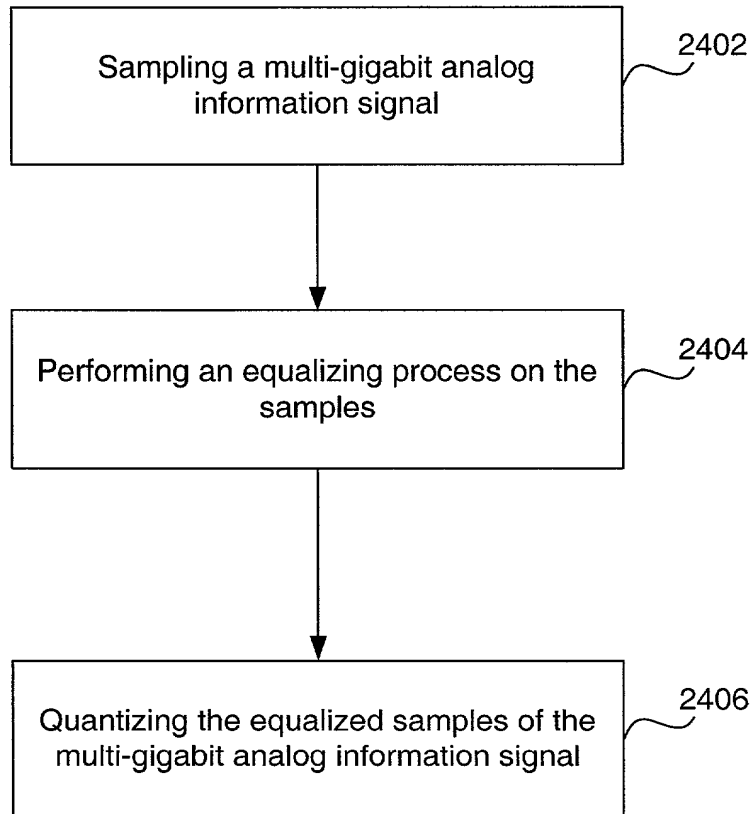


FIG. 24

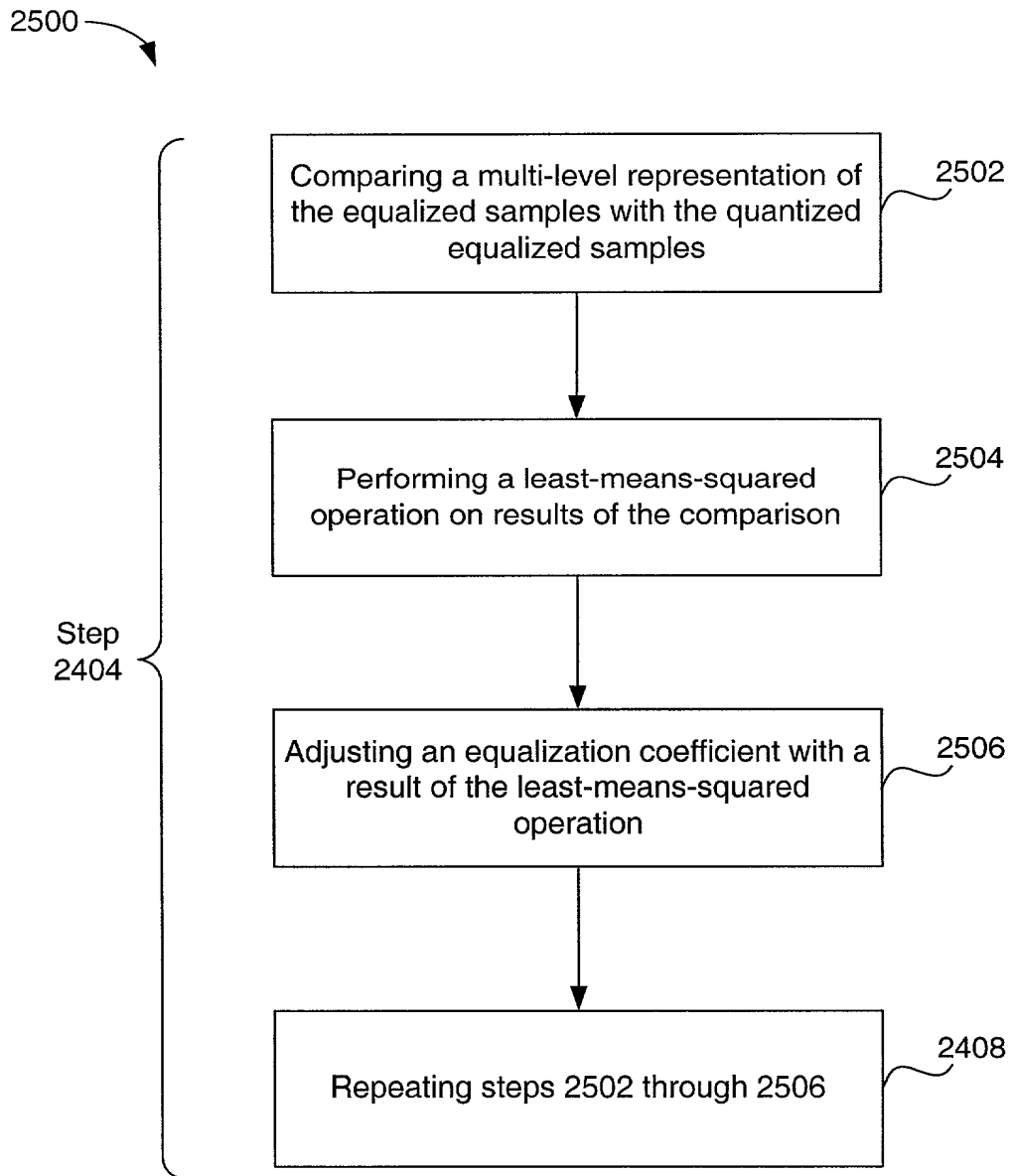




FIG. 25

2600



Step
2404



Minimizing differences between post-
transition sample amplitudes and steady
state sample amplitudes of the samples

2602



FIG. 26

2700

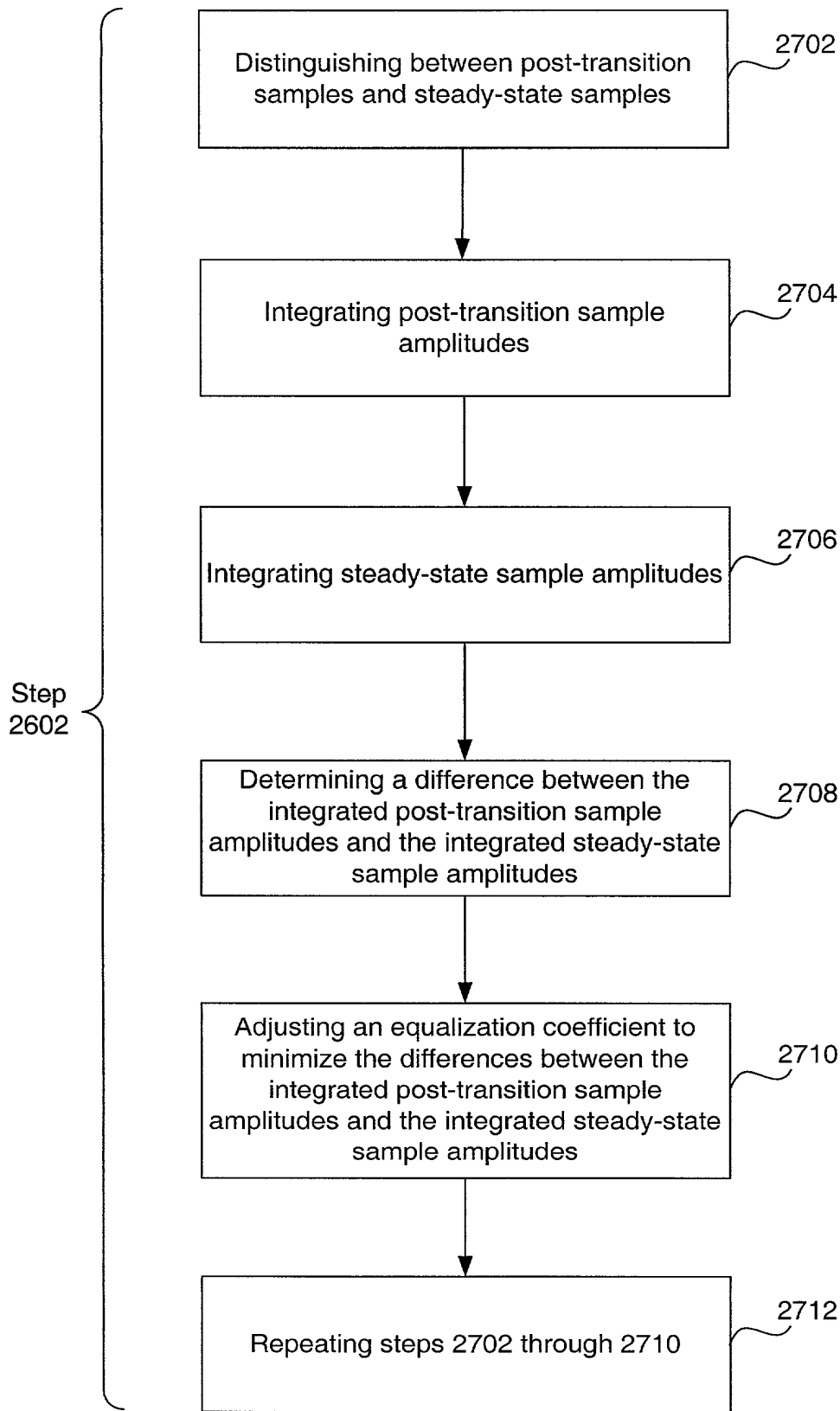


FIG. 27

2800

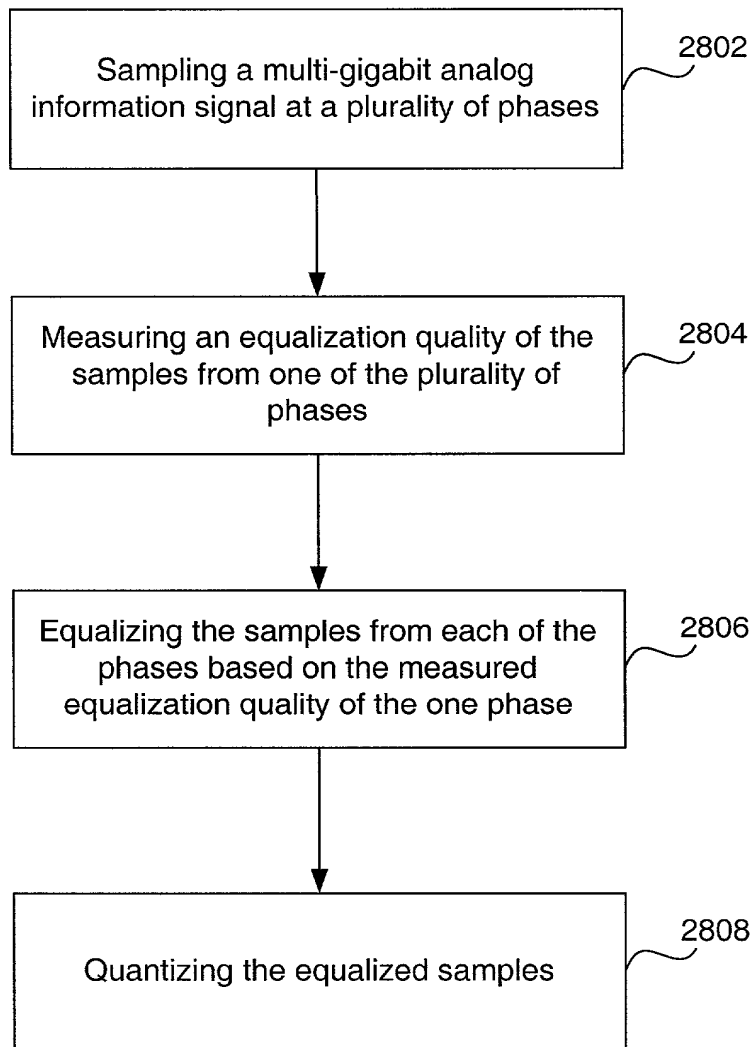


FIG. 28

2900

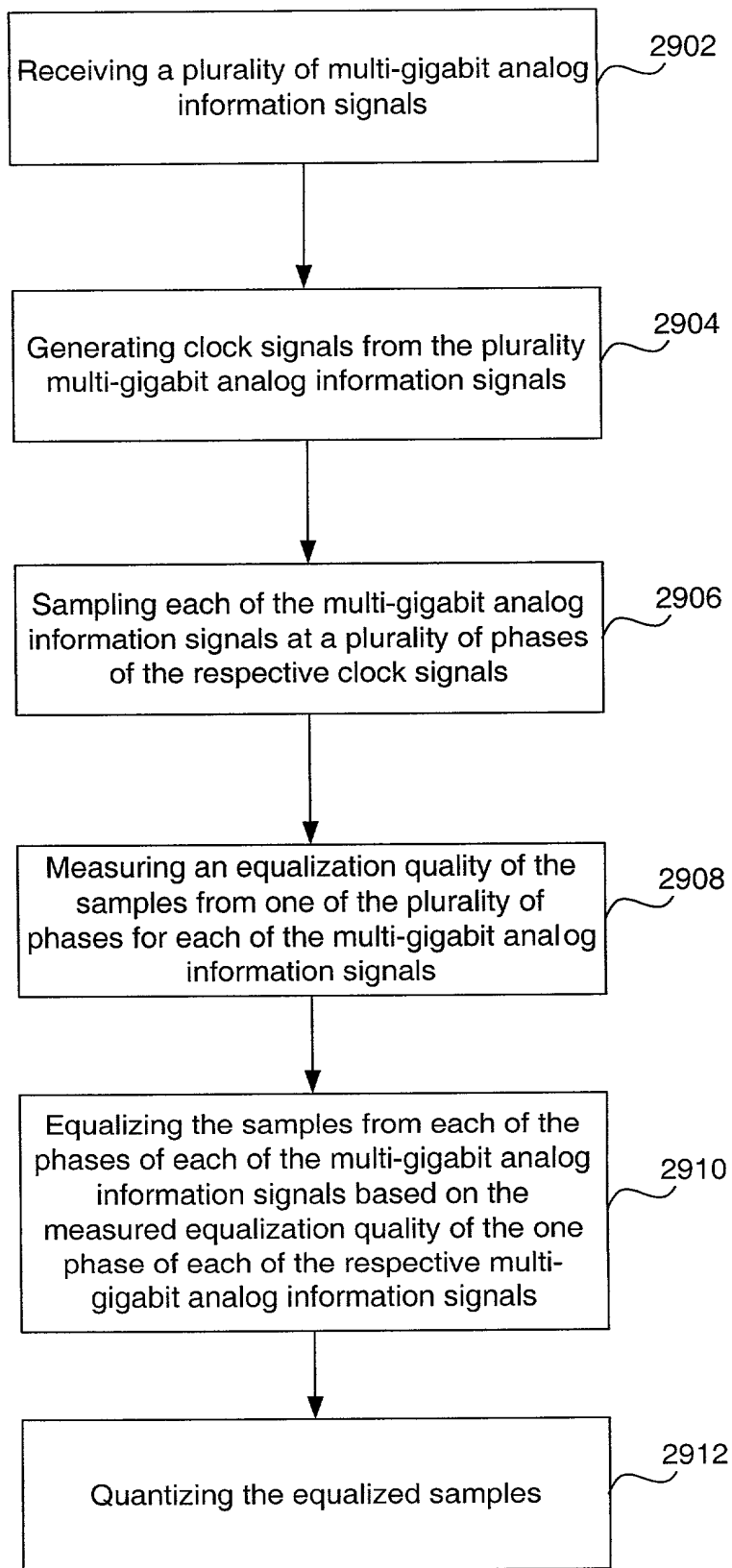


FIG. 29

3000

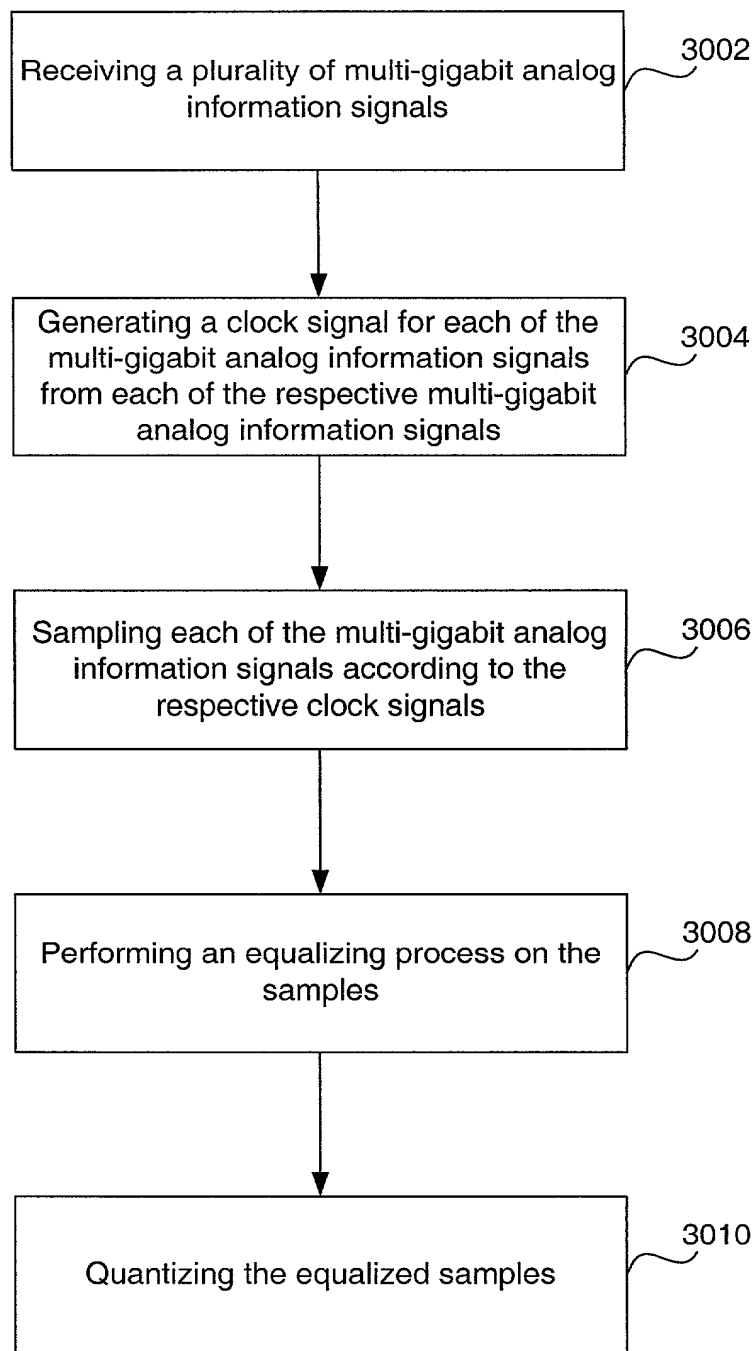


FIG. 30